

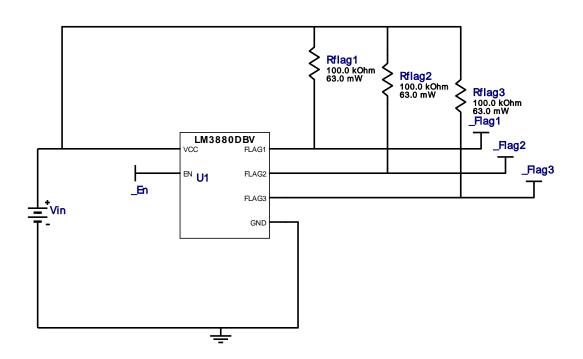
WEBENCH® Design Report

 $\begin{aligned} & \text{VinMin} = 14.0 \text{V} \\ & \text{VinMax} = 22.0 \text{V} \\ & \text{Vout} = 3.3 \text{V} \\ & \text{Iout} = 2.0 \text{A} \end{aligned}$ 

Device = LM3880MF-1AE/NOPB Topology = SEQUENCER Created = 9/22/16 8:06:39 PM BOM Cost = \$0.48 BOM Count = 4 Total Pd = 0.0W

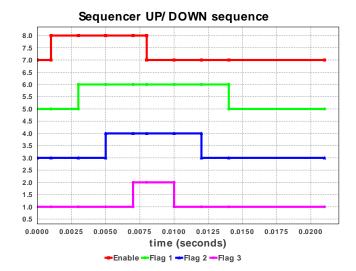
Design: 4800879/3 LM3880MF-1AE/NOPB

Design 3 - LM3880MF-1AE/NOPB



## **Electrical BOM**

#	Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
1.	Rflag1	Vishay-Dale	CRCW0402100KFKED Series= CRCWe3	Res= 100.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm <sup>2</sup>
2.	Rflag2	Vishay-Dale	CRCW0402100KFKED Series= CRCWe3	Res= 100.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm <sup>2</sup>
3.	Rflag3	Vishay-Dale	CRCW0402100KFKED Series= CRCWe3	Res= 100.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm <sup>2</sup>
4.	U1	Texas Instruments	LM3880MF-1AE/NOPB	Switcher	1	\$0.45	R-PDSO-G6 10 mm <sup>2</sup>



## **Operating Values**

#	Name	Value	Category	Description
1.	BOM Count	4	General	Total Design BOM count
2.	FootPrint	19.0 mm <sup>2</sup>	General	Total Foot Print Area of BOM components
3.	Total BOM	\$0.48	General	Total BOM Cost
4.	Total Pd	75.0 μW	Power	Total Power Dissipation
5.	Flag Voltage	3.0 V		Flag Voltage
6.	Flag1 Down delay	6.0 ms		Flag Delay
	(From EN high to low)			
7.	- 3 - 1 7 (	2.0 ms		Flag Delay
	EN low to high)			
8.	Flag2 Down delay	4.0 ms		Flag Delay
_	(From EN high to low)			
9.	Flag2 Up delay (From	4.0 ms		Flag Delay
4.0	EN low to high)	0.0		Fi
10.	Flag3 Down delay	2.0 ms		Flag Delay
44	(From EN high to low)	6.0 ma		Flog Dolov
11.	Flag3 Up delay (From EN low to high)	6.0 ms		Flag Delay
12.	Flags Used	2.0		Flags Used
13.	Total Flags	3.0		Total Flags
14.	Vcc	3.0 V		Vcc
	V 00	0.0 V		V 00

## Design Inputs

#	Name	Value	Description
5.	base_pn	LM3880	Base Product Number

## Design Assistance

 $1. \ \textbf{LM3880} \ Product \ Folder: http://www.ti.com/product/LM3880: contains \ the \ data \ sheet \ and \ other \ resources.$ 

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