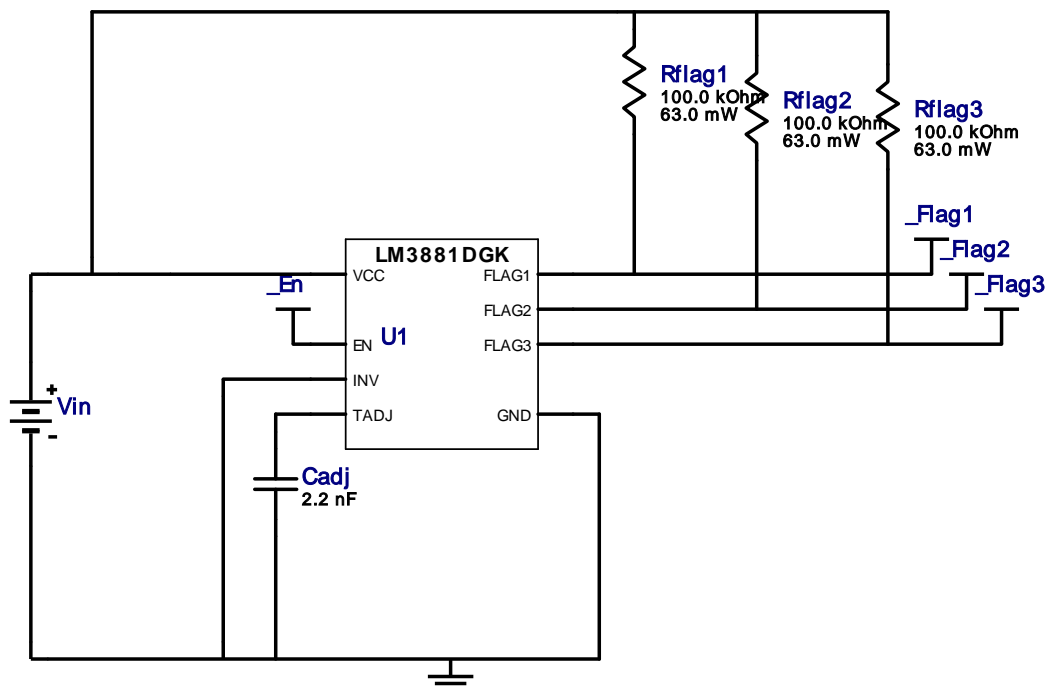







## WEBENCH® Design Report

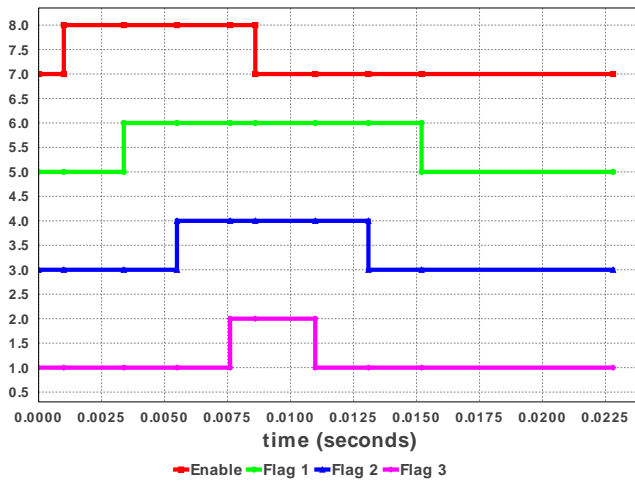
Design : 4799748/5 LM3881MM/NOPB  
Design 5 - LM3881MM/NOPB



### Electrical BOM

#	Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
1.	Cadj	Yageo America	CC0805KRX7R9BB222 Series= X7R	Cap= 2.2 nF VDC= 50.0 V IRMS= 0.0 A	1	\$0.01	 0805 7 mm <sup>2</sup>
2.	Rflag1	Vishay-Dale	CRCW0402100KFKED Series= CRCW..e3	Res= 100.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm <sup>2</sup>
3.	Rflag2	Vishay-Dale	CRCW0402100KFKED Series= CRCW..e3	Res= 100.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm <sup>2</sup>
4.	Rflag3	Vishay-Dale	CRCW0402100KFKED Series= CRCW..e3	Res= 100.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm <sup>2</sup>
5.	U1	Texas Instruments	LM3881MM/NOPB	Switcher	1	\$0.50	 S-PDSO-G8 36 mm <sup>2</sup>

Sequencer UP/ DOWN sequence



## Operating Values

#	Name	Value	Category	Description
1.	BOM Count	5	General	Total Design BOM count
2.	FootPrint	52.0 mm <sup>2</sup>	General	Total Foot Print Area of BOM components
3.	Total BOM	\$0.54	General	Total BOM Cost
4.	Total Pd	240.0 $\mu$ W	Power	Total Power Dissipation
5.	Flag Voltage	3.0 V		Flag Voltage
6.	Flag1 Down delay (From EN high to low)	6.6 ms		Flag Delay
7.	Flag1 Up delay (From EN low to high)	2.376 ms		Flag Delay
8.	Flag2 Down delay (From EN high to low)	4.488 ms		Flag Delay
9.	Flag2 Up delay (From EN low to high)	4.488 ms		Flag Delay
10.	Flag3 Down delay (From EN high to low)	2.376 ms		Flag Delay
11.	Flag3 Up delay (From EN low to high)	6.6 ms		Flag Delay
12.	Flags Used	2.0		Flags Used
13.	Total Flags	3.0		Total Flags
14.	Vcc	3.0 V		Vcc

## Design Inputs

#	Name	Value	Description
5.	base_pn	LM3881	Base Product Number

## Design Assistance

1. **LM3881** Product Folder : <http://www.ti.com/product/LM3881> : contains the data sheet and other resources.

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