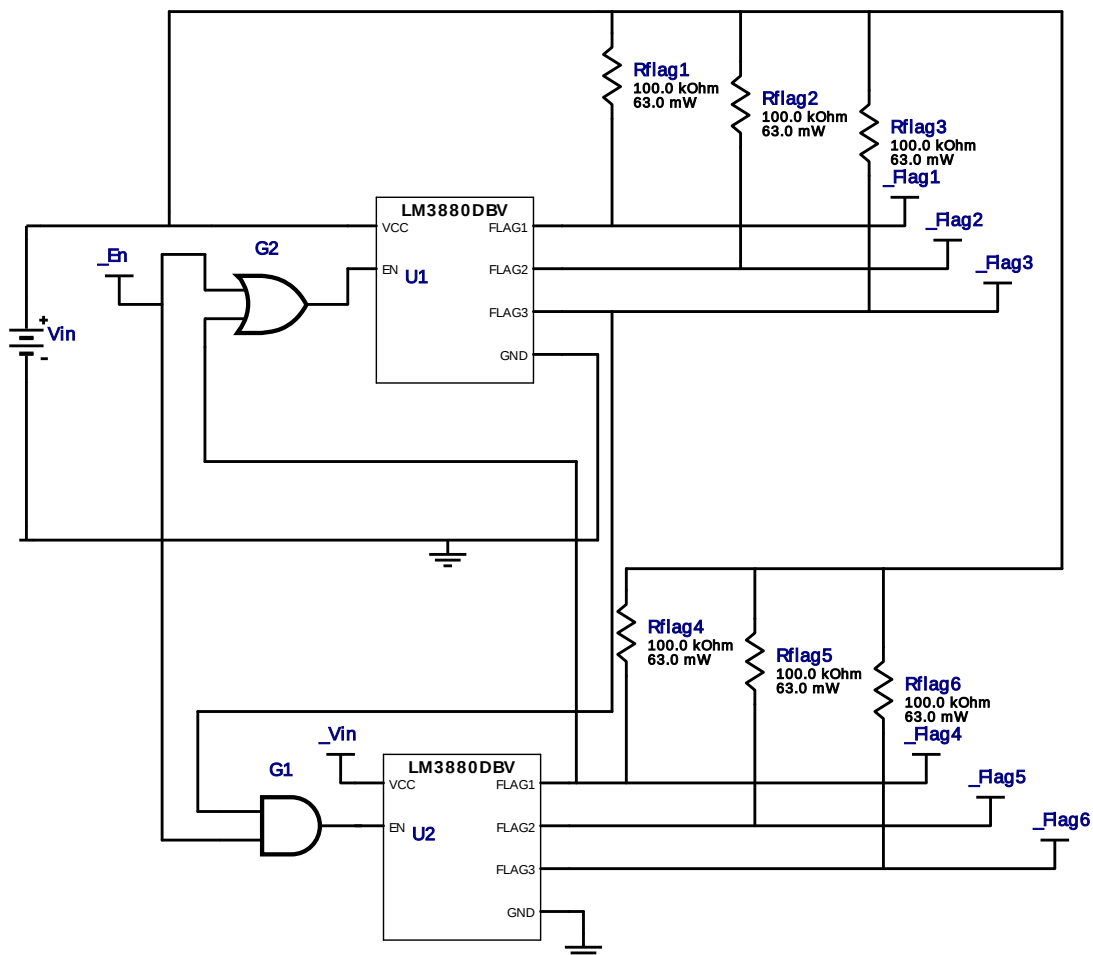


WEBENCH® Design Report

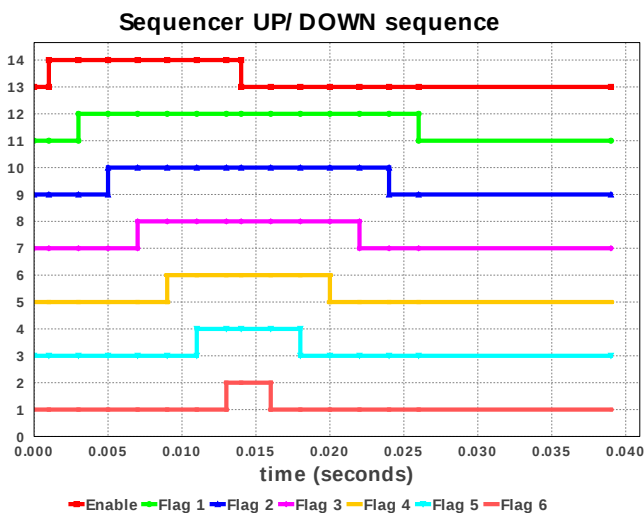
Design : 4792759/3 LM3880MF-1AE/NOPB
 Design 3 - LM3880MF-1AE/NOPB



Electrical BOM

#	Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
1.	G1	Texas Instruments	SN74AHC1G08	LogicGate	1	\$0.06	 SOT-23 14 mm²
2.	G2	Texas Instruments	SN74AHC1G32	LogicGate	1	\$0.07	 SOT-23 14 mm²
3.	Rflag1	Vishay-Dale	CRCW0402100KFKED Series= CRCW..e3	Res= 100.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm²
4.	Rflag2	Vishay-Dale	CRCW0402100KFKED Series= CRCW..e3	Res= 100.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm²
5.	Rflag3	Vishay-Dale	CRCW0402100KFKED Series= CRCW..e3	Res= 100.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm²

#	Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
6.	Rflag4	Vishay-Dale	CRCW0402100KFKED Series= CRCW..e3	Res= 100.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm ²
7.	Rflag5	Vishay-Dale	CRCW0402100KFKED Series= CRCW..e3	Res= 100.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm ²
8.	Rflag6	Vishay-Dale	CRCW0402100KFKED Series= CRCW..e3	Res= 100.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm ²
9.	U1	Texas Instruments	LM3880MF-1AE/NOPB	Switcher	1	\$0.45	 R-PDSO-G6 10 mm ²
10.	U2	Texas Instruments	LM3880MF-1AE/NOPB	Switcher	1	\$0.45	 R-PDSO-G6 10 mm ²



Operating Values

#	Name	Value	Category	Description
1.	BOM Count	10	General	Total Design BOM count
2.	FootPrint	65.0 mm ²	General	Total Foot Print Area of BOM components
3.	Total BOM	\$1.09	General	Total BOM Cost
4.	Total Pd	150.0 μ W	Power	Total Power Dissipation
5.	Flag Voltage	3.0 V		Flag Voltage
6.	Flag1 Down delay (From EN high to low)	12.0 ms		Flag Delay
7.	Flag1 Up delay (From EN low to high)	2.0 ms		Flag Delay
8.	Flag2 Down delay (From EN high to low)	10.0 ms		Flag Delay
9.	Flag2 Up delay (From EN low to high)	4.0 ms		Flag Delay
10.	Flag3 Down delay (From EN high to low)	8.0 ms		Flag Delay
11.	Flag3 Up delay (From EN low to high)	6.0 ms		Flag Delay
12.	Flag4 Down delay (From EN high to low)	6.0 ms		Flag Delay
13.	Flag4 Up delay (From EN low to high)	8.0 ms		Flag Delay

#	Name	Value	Category	Description
14.	Flag5 Down delay (From EN high to low)	4.0 ms		Flag Delay
15.	Flag5 Up delay (From EN low to high)	10.0 ms		Flag Delay
16.	Flag6 Down delay (From EN high to low)	2.0 ms		Flag Delay
17.	Flag6 Up delay (From EN low to high)	12.0 ms		Flag Delay
18.	Flags Used	6.0		Flags Used
19.	Total Flags	6.0		Total Flags
20.	Vcc	3.0 V		Vcc

Design Inputs

#	Name	Value	Description
5.	base_pn	LM3880	Base Product Number

Design Assistance

1. **LM3880** Product Folder : <http://www.ti.com/product/LM3880> : contains the data sheet and other resources.

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