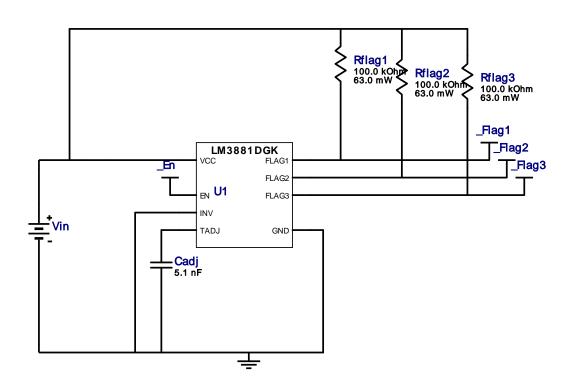


WEBENCH® Design Report

VinMin = 14.0V VinMax = 22.0V Vout = 3.3V Iout = 2.0A Device = LM3881MM/NOPB Topology = SEQUENCER Created = 7/28/16 10:07:34 PM BOM Cost = \$0.57 BOM Count = 5 Total Pd = 0.0W

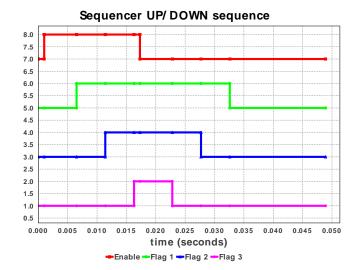
Design: 4755129/9 LM3881MM/NOPB

Design 9 - LM3881MM/NOPB



## **Electrical BOM**

#	Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
1.	Cadj	MuRata	GRM2195C1H512JA01D Series= C0G/NP0	Cap= 5.1 nF VDC= 50.0 V IRMS= 0.0 A	1	\$0.04	0805 7 mm <sup>2</sup>
2.	Rflag1	Vishay-Dale	CRCW0402100KFKED Series= CRCWe3	Res= 100.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm <sup>2</sup>
3.	Rflag2	Vishay-Dale	CRCW0402100KFKED Series= CRCWe3	Res= 100.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm <sup>2</sup>
4.	Rflag3	Vishay-Dale	CRCW0402100KFKED Series= CRCWe3	Res= 100.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm <sup>2</sup>
5.	U1	Texas Instruments	LM3881MM/NOPB	Switcher	1	\$0.50	S-PDSO-G8 36 mm <sup>2</sup>



## **Operating Values**

- 1	3			
#	Name	Value	Category	Description
1.	BOM Count	5	General	Total Design BOM count
2.	FootPrint	52.0 mm <sup>2</sup>	General	Total Foot Print Area of BOM components
3.	Total BOM	\$0.57	General	Total BOM Cost
4.	Total Pd	264.0 μW	Power	Total Power Dissipation
5.	Flag Voltage	3.3 V		Flag Voltage
6.	Flag1 Down delay	15.3 ms		Flag Delay
	(From EN high to low)			
7.	Flag1 Up delay (From	5.508 ms		Flag Delay
	EN low to high)			
8.	Flag2 Down delay	10.404 ms		Flag Delay
_	(From EN high to low)			
9.	Flag2 Up delay (From	10.404 ms		Flag Delay
40	EN low to high)	5 500		Flori Dolor
10.	Flag3 Down delay	5.508 ms		Flag Delay
4.4	(From EN high to low)	15 2 ma		Flog Polov
11.	Flag3 Up delay (From	15.3 ms		Flag Delay
12.	EN low to high) Flags Used	2.0		Flags Used
13.	Total Flags	3.0		Total Flags
14.	Vcc	3.3 V		Vcc
14.	V 0.0	J.J V		V CC

## **Design Inputs**

#	Name	Value	Description
5.	base pn	LM3881	Base Product Number

## Design Assistance

1. LM3881 Product Folder: http://www.ti.com/product/LM3881: contains the data sheet and other resources.

Texas Instruments' WEBENCH simulation tools attempt to recreate the performance of a substantially equivalent physical implementation of the design. Simulations are created using Texas Instruments' published specifications as well as the published specifications of other device manufacturers. While Texas Instruments does update this information periodically, this information may not be current at the time the simulation is built. Texas Instruments does not warrant the accuracy or completeness of the specifications or any information contained therein. Texas Instruments does not warrant that any designs or recommended parts will meet the specifications you entered, will be suitable for your application or fit for any particular purpose, or will operate as shown in the simulation in a physical implementation. Texas Instruments does not warrant that the designs are production worthy.

You should completely validate and test your design implementation to confirm the system functionality for your application prior to production.

Use of Texas Instruments' WEBENCH simulation tools is subject to Texas Instruments' Site Terms and Conditions of Use. Prototype boards based on WEBENCH created designs are provided AS IS without warranty of any kind for evaluation and testing purposes and are subject to the terms of the Evaluation License Agreement.