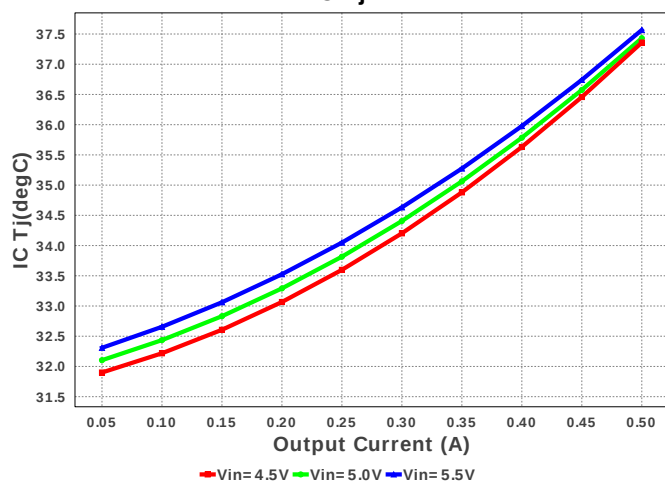




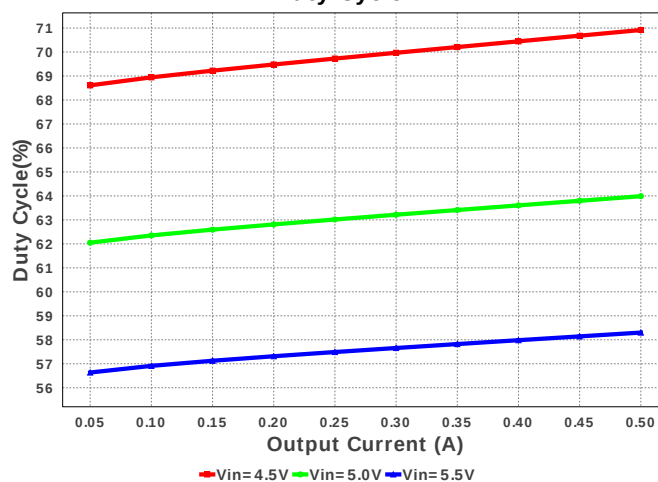
Device = LM34919CQTLX/NOPB
Topology = Buck
Created = 7/19/16 6:03:44 AM
BOM Cost = \$1.86
BOM Count = 16
Total Pd = 0.26W

#	Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
11.	Rfb1	Vishay-Dale	CRCW04021K00FKED Series= CRCW..e3	Res= 1000.0 Ohm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
12.	Rfb2	Vishay-Dale	CRCW0402200RFKED Series= CRCW..e3	Res= 200.0 Ohm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
13.	Rpg	Vishay-Dale	CRCW040210K0FKED Series= CRCW..e3	Res= 10.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
14.	Rr	Vishay-Dale	CRCW04029K09FKED Series= CRCW..e3	Res= 9.09 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
15.	Rt	Vishay-Dale	CRCW040297K6FKED Series= CRCW..e3	Res= 97.6 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
16.	U1	Texas Instruments	LM34919CQTLX/NOPB	Switcher	1	\$1.20	TLA12LDA 9 mm ²

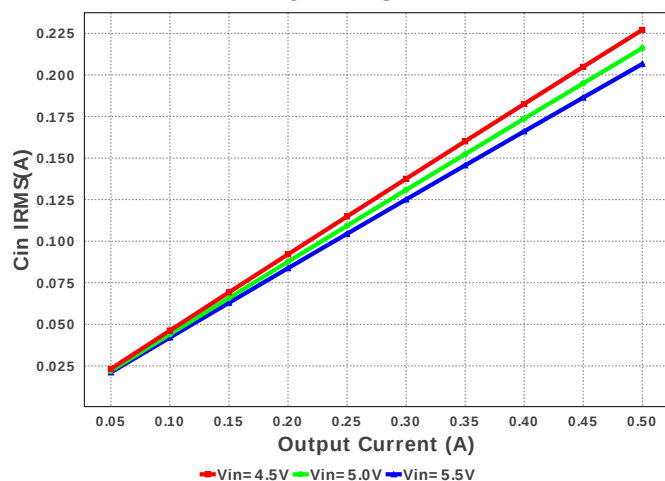
IC Tj



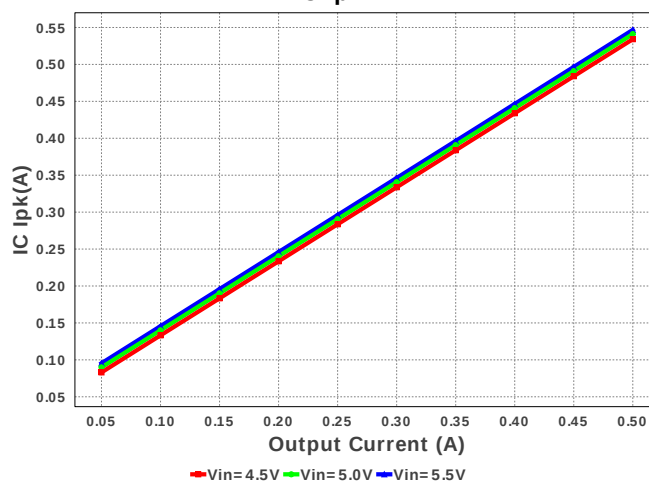
Duty Cycle

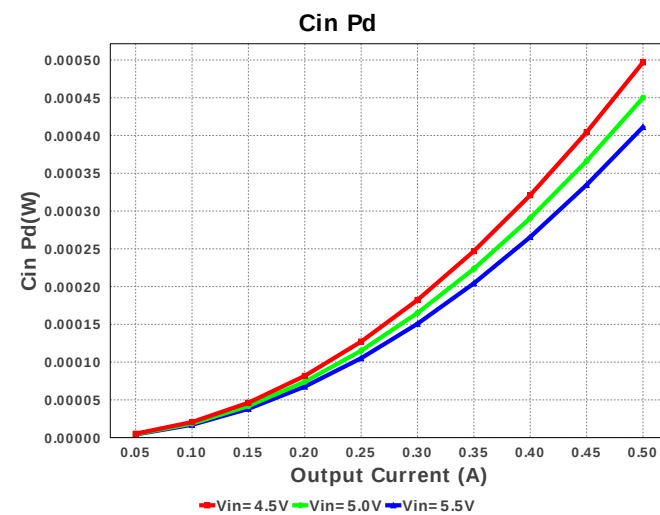
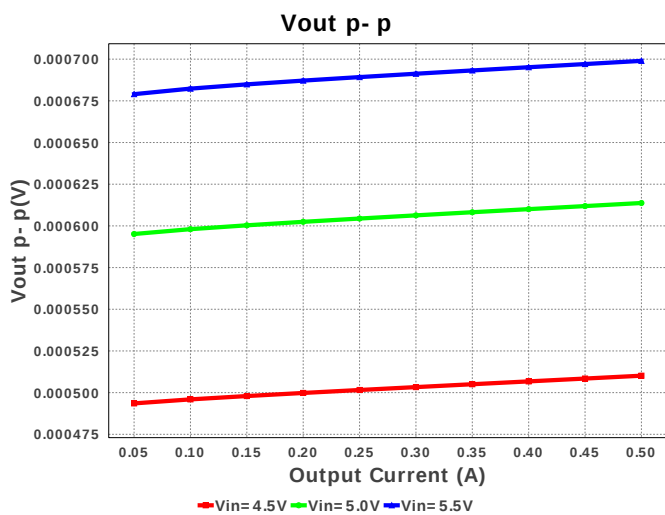
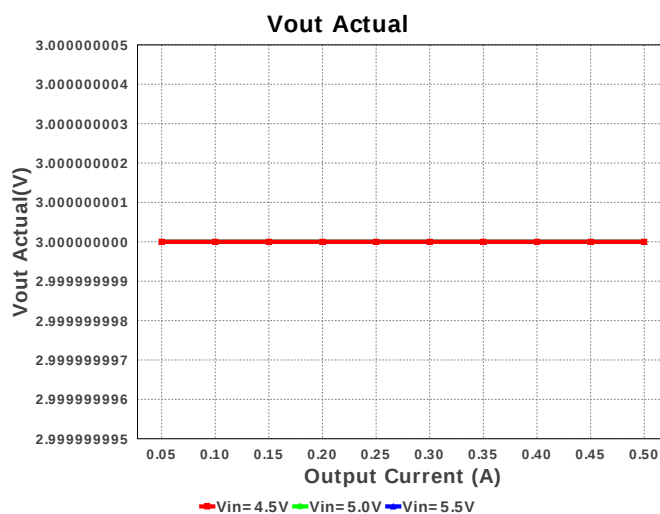
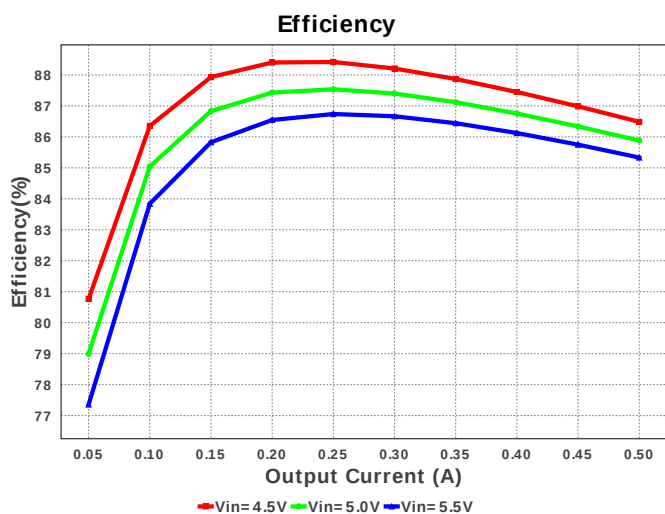
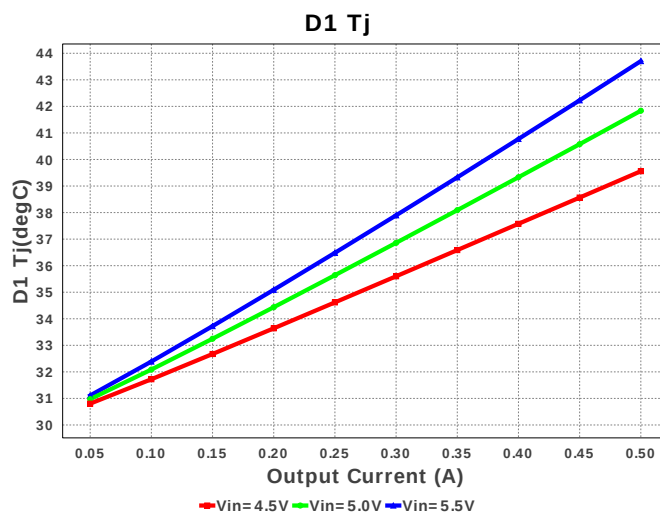
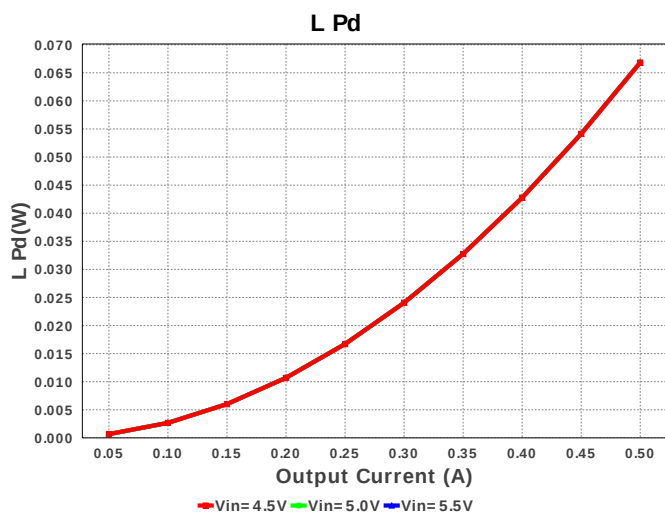


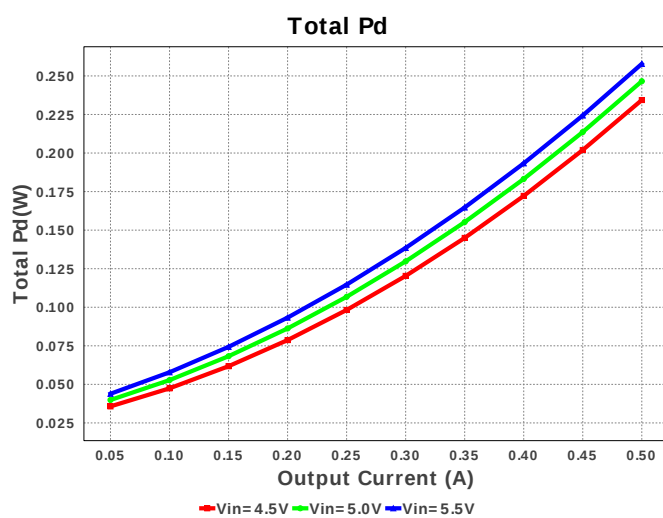
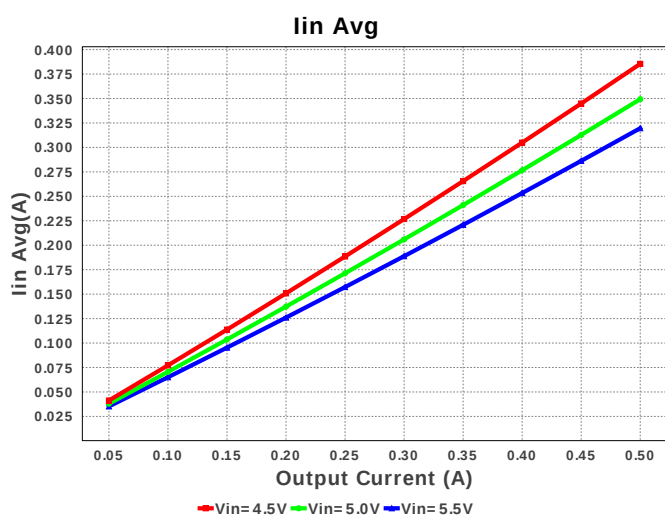
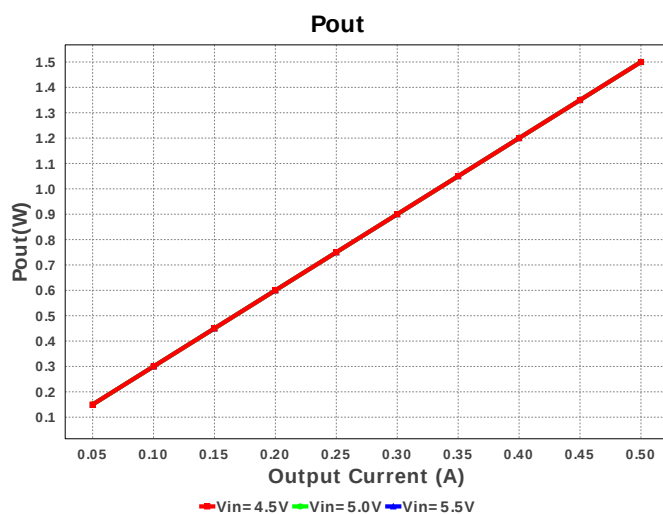
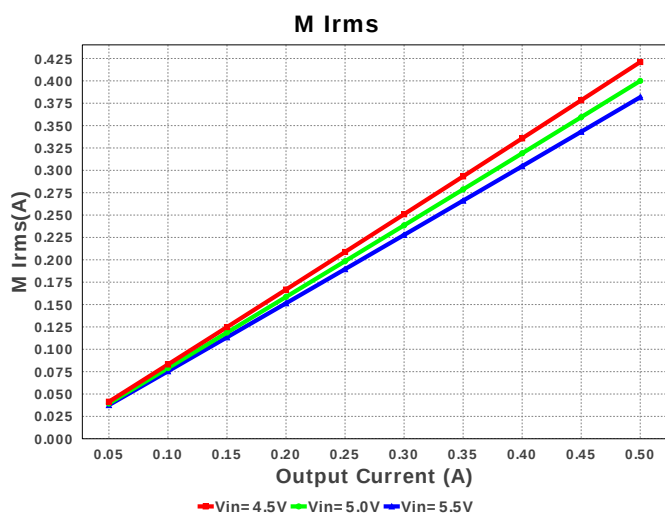
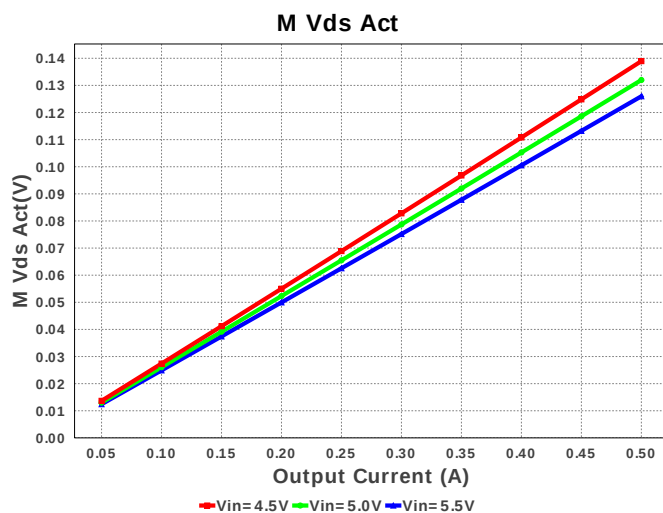
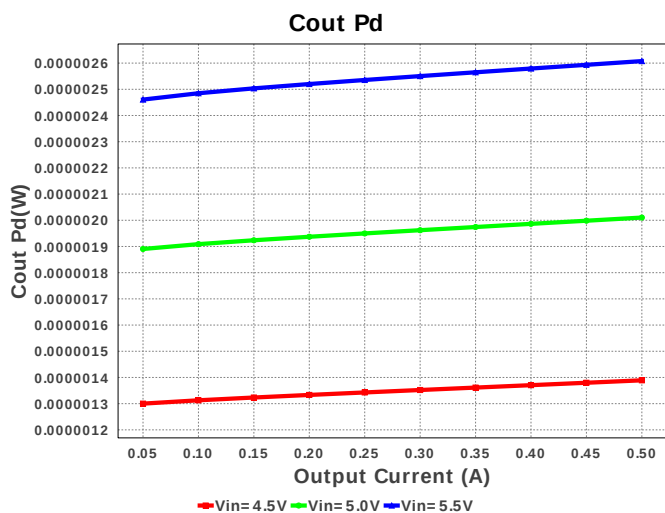
Cin IRMS

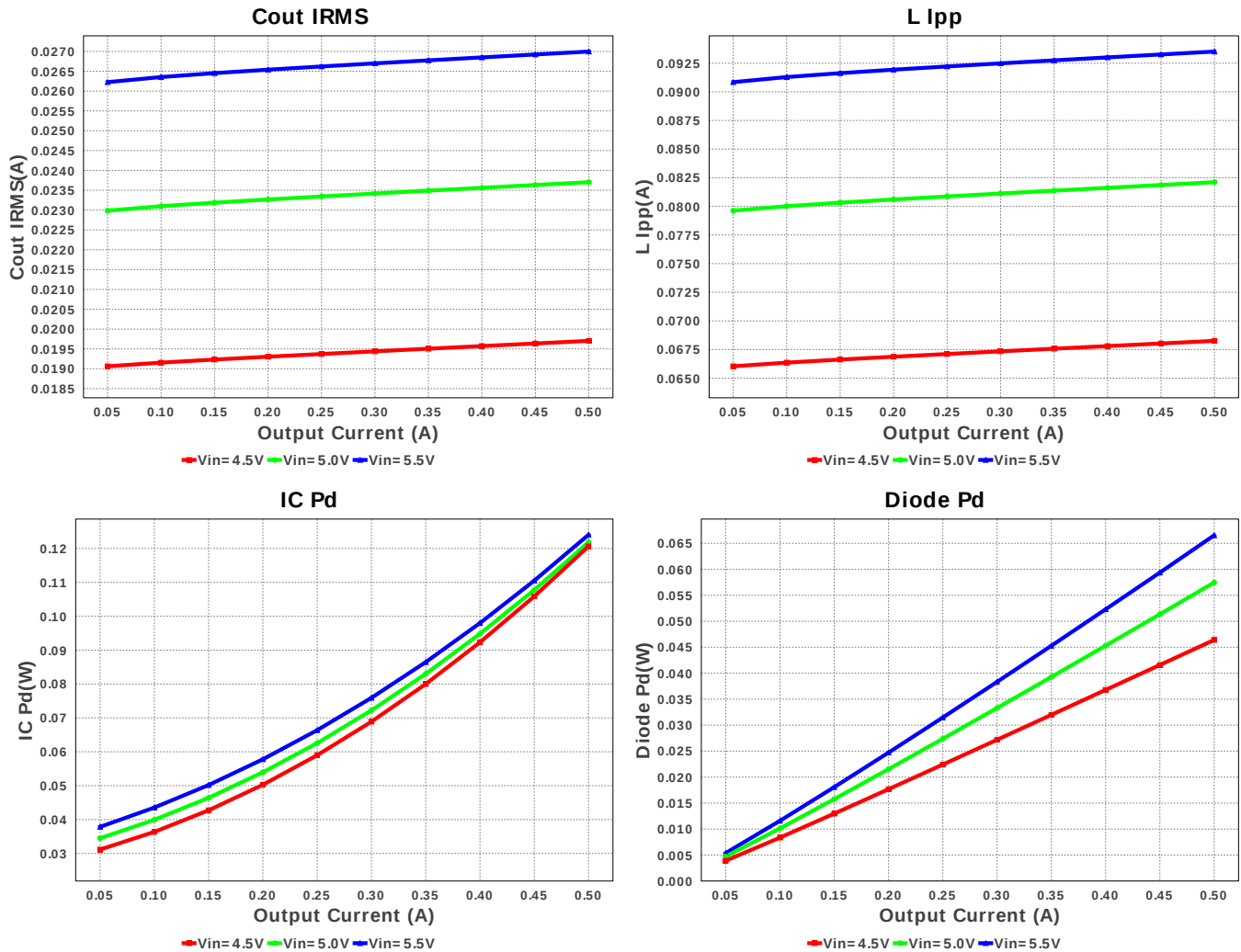


IC Ipk









Operating Values

#	Name	Value	Category	Description
1.	Cin IRMS	206.571 mA	Current	Input capacitor RMS ripple current
2.	Cout IRMS	26.996 mA	Current	Output capacitor RMS ripple current
3.	IC Ipk	546.759 mA	Current	Peak switch current in IC
4.	Iin Avg	319.6 mA	Current	Average input current
5.	L Ipp	93.518 mA	Current	Peak-to-peak inductor ripple current
6.	M1 Irms	381.773 mA	Current	Q lavg
7.	BOM Count	16	General	Total Design BOM count
8.	FootPrint	98.0 mm ²	General	Total Foot Print Area of BOM components
9.	Frequency	865.851 kHz	General	Switching frequency
10.	IC Tolerance	50.0 mV	General	IC Feedback Tolerance
11.	M Vds Act	125.985 mV	General	Voltage drop across the MosFET
12.	Pout	1.5 W	General	Total output power
13.	Total BOM	\$1.86	General	Total BOM Cost
14.	D1 Tj	43.705 degC	Op_Point	D1 junction temperature
15.	Vout Actual	3.0 V	Op_Point	Vout Actual calculated based on selected voltage divider resistors
16.	Vout OP	3.0 V	Op_Point	Operational Output Voltage
17.	Duty Cycle	58.3 %	Op_point	Duty cycle
18.	Efficiency	85.333 %	Op_point	Steady state efficiency
19.	IC Tj	37.566 degC	Op_point	IC junction temperature
20.	ICThetaJA	61.0 degC/W	Op_point	IC junction-to-ambient thermal resistance
21.	IOUT_OP	500.0 mA	Op_point	Iout operating point
22.	VIN_OP	5.5 V	Op_point	Vin operating point
23.	Vout p-p	698.97 μ V	Op_point	Peak-to-peak output ripple voltage
24.	Cin Pd	411.228 μ W	Power	Input capacitor power dissipation
25.	Cout Pd	2.608 μ W	Power	Output capacitor power dissipation
26.	Diode Pd	66.531 mW	Power	Diode power dissipation
27.	IC Pd	124.04 mW	Power	IC power dissipation
28.	L Pd	66.825 mW	Power	Inductor power dissipation
29.	Total Pd	257.817 mW	Power	Total Power Dissipation
30.	Vout Tolerance	2.343 %		Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable

Design Inputs

#	Name	Value	Description
1.	Iout	500.0 m	Maximum Output Current
2.	VinMax	5.5	Maximum input voltage
3.	VinMin	4.5	Minimum input voltage
4.	Vout	3.0	Output Voltage
5.	base_pn	LM34919C-Q1	Base Product Number
6.	source	DC	Input Source Type
7.	Ta	30.0	Ambient temperature

Design Assistance

1. For a Constant On Time device to be stable, we need to provide a ripple at the feedback comparator. There are various methods to implement the ripple. Depending on the circuit complexity vs. the allowable ripple, we have three options to choose from. The simplest option, 'Low Complexity', would require only a high ESR cap at the output. This means that the BOM count will be small, but the output voltage ripple will be quite large. The 'optimal solution' would require a feed-forward cap in parallel with the upper feedback resistor to AC couple the ripple to the feedback node. This increases the BOM count slightly, but now we have more control over the output voltage ripple. If the output voltage requirement is very tight, then the best option is to go for the 'Low Output Ripple' solution. In this option we can go with very low ESR output caps and have very good control over the output voltage ripple.

2. Feature Highlights: Automotive Qualified 12V to 14V Vin, 600mA Constant On-Time Buck Regulator.

3. The LM34919C-Q1 is qualified for Automotive applications. All passives and other components selected in this design may not be qualified for Automotive applications. The user is required to verify that all components in the design meet the qualification and safety requirements for their specific application.

4. **LM34919C-Q1** Product Folder : <http://www.ti.com/product/LM34919C%2DQ1> : contains the data sheet and other resources.

Texas Instruments' WEBENCH simulation tools attempt to recreate the performance of a substantially equivalent physical implementation of the design. Simulations are created using Texas Instruments' published specifications as well as the published specifications of other device manufacturers. While Texas Instruments does update this information periodically, this information may not be current at the time the simulation is built. Texas Instruments does not warrant the accuracy or completeness of the specifications or any information contained therein. Texas Instruments does not warrant that any designs or recommended parts will meet the specifications you entered, will be suitable for your application or fit for any particular purpose, or will operate as shown in the simulation in a physical implementation. Texas Instruments does not warrant that the designs are production worthy.

You should completely validate and test your design implementation to confirm the system functionality for your application prior to production.

Use of Texas Instruments' WEBENCH simulation tools is subject to [Texas Instruments' Site Terms and Conditions of Use](#). Prototype boards based on WEBENCH created designs are provided AS IS without warranty of any kind for evaluation and testing purposes and are subject to the terms of the [Evaluation License Agreement](#).