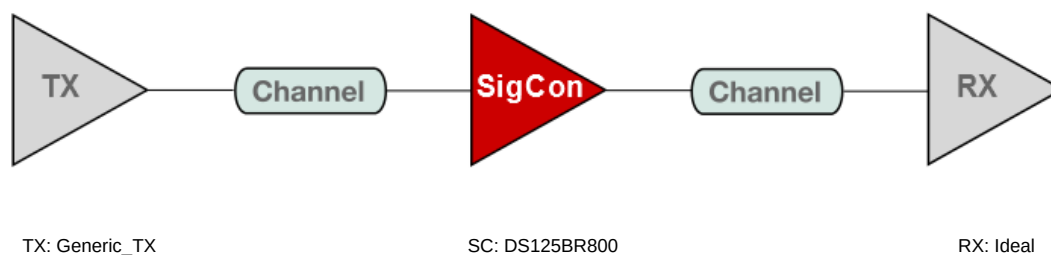


WEBENCH[®] Interface Report

Design : 1194016/2204

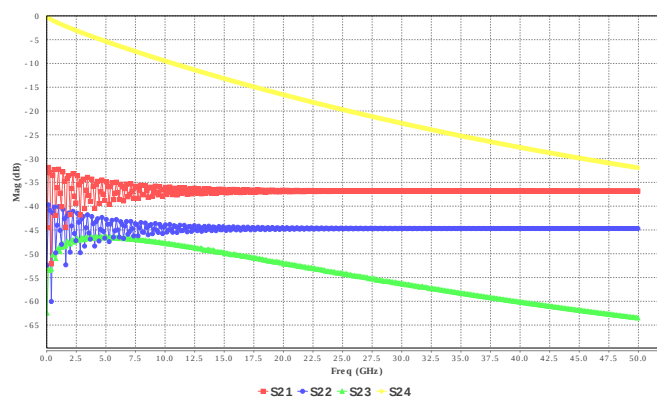
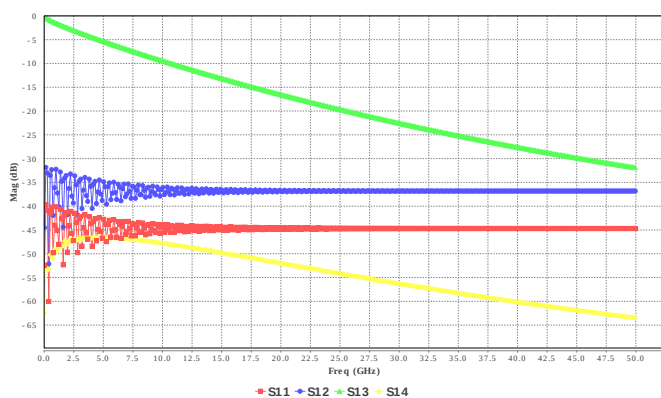
System Block Diagram

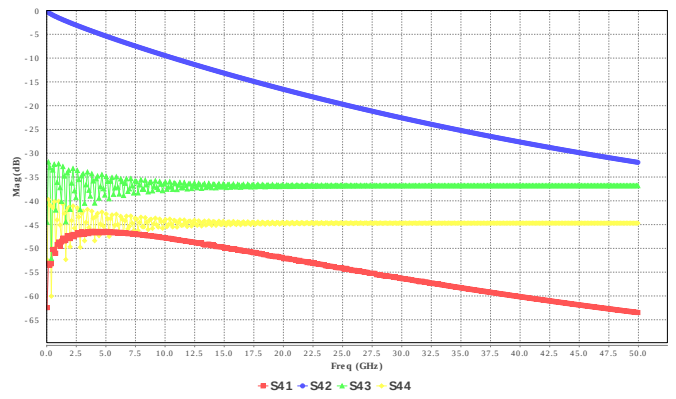
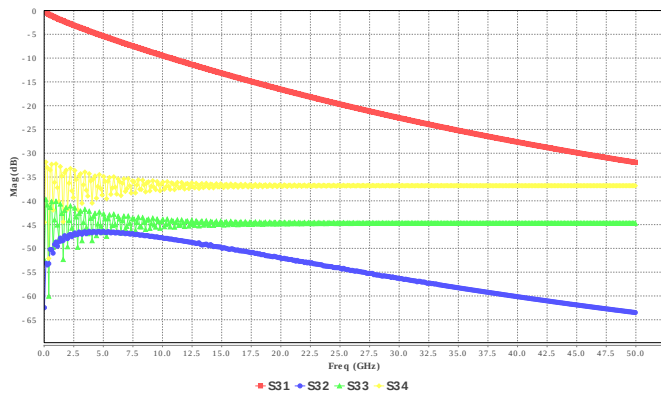


Transmission Medium

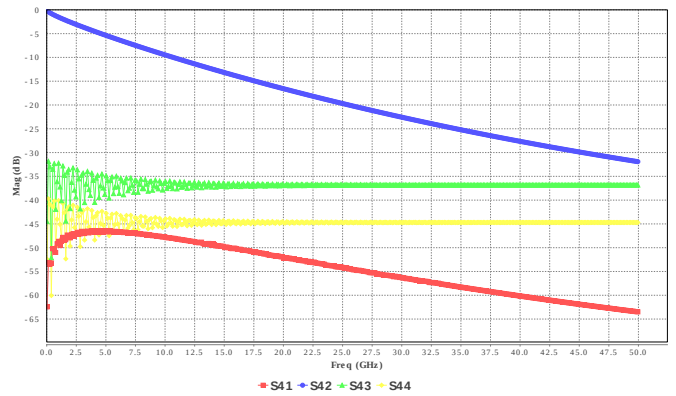
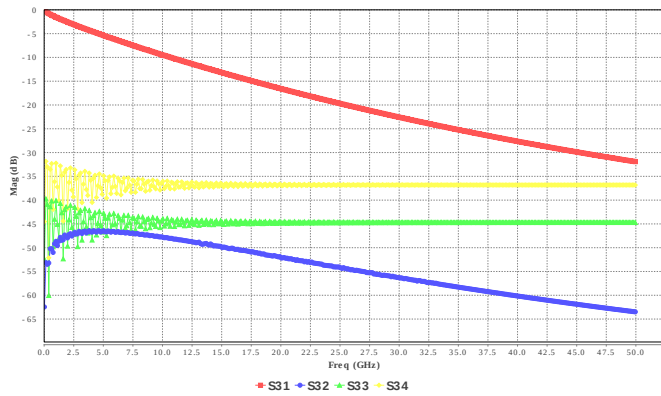
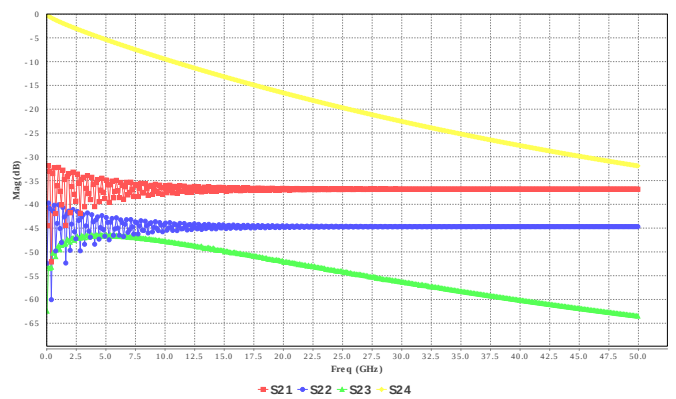
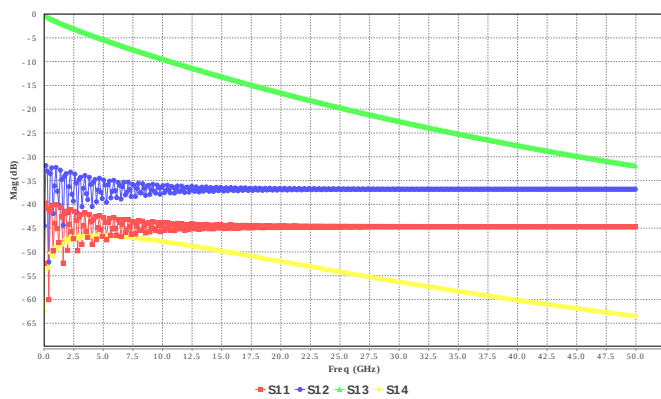


Channel1





Channel2



Simulation Settings

General Parameters:

Data Rate: 1.0E9 Number of UI: 10000.0
SamplesPerBit: 32 Data Pattern: Squire Wave
Jitter RMS: 0.0 BER Extrapolation: false

TX Parameters

#	Name	Value
1.	SlewTime	10
2.	Pre	0
3.	Pst1	0
4.	Pst2	0
5.	Swing	1000

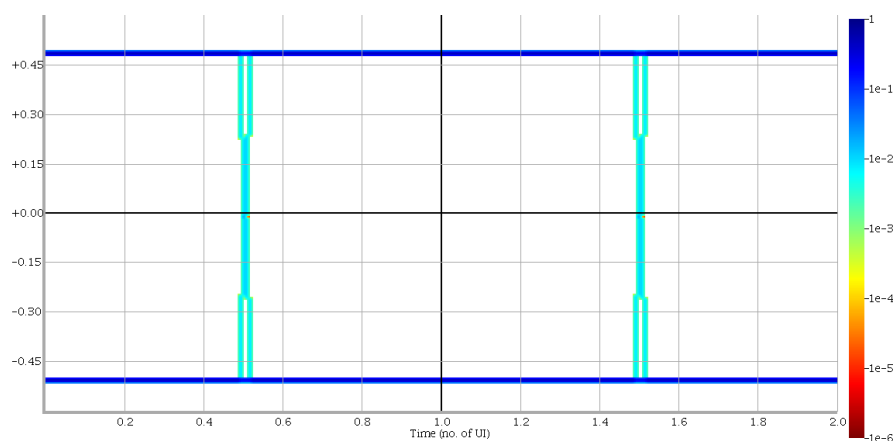
SC RX Parameters

#	Name	Value
1.	EQ_Level	0
2.	Limit	1
3.	LTI_mode	0

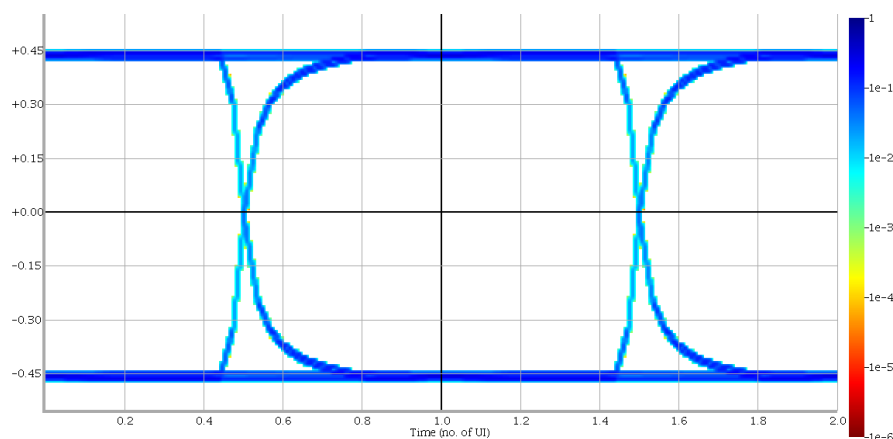
SC TX Parameters

#	Name	Value
1.	VOD_Level	7
2.	DE_Level	0
3.	gain_debugonly	0.90
4.	LTI_mode	0

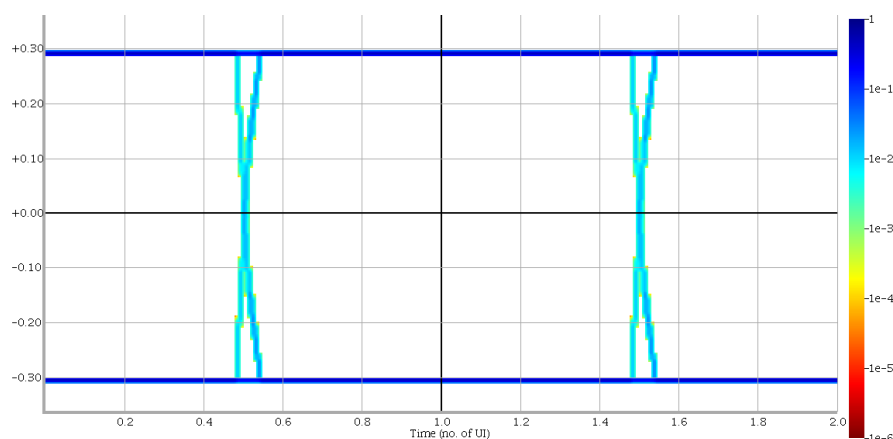
Simulation Results



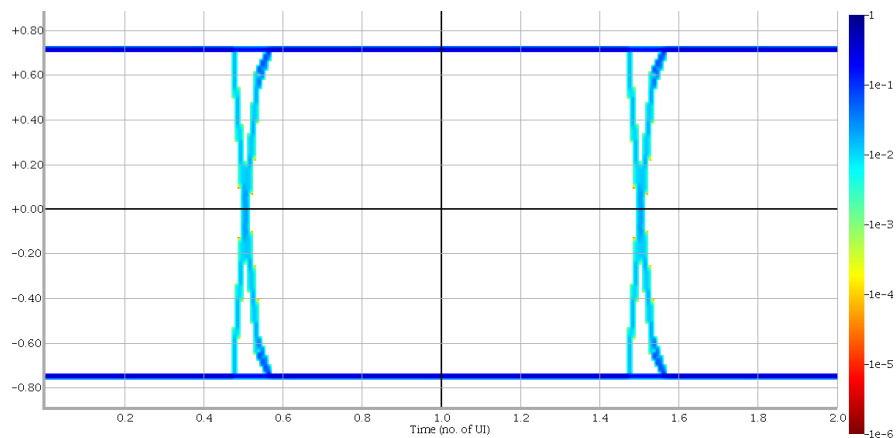
Name: TX_OUT_PT1 Height:0.984 Width:0.984 Jitter:0.016



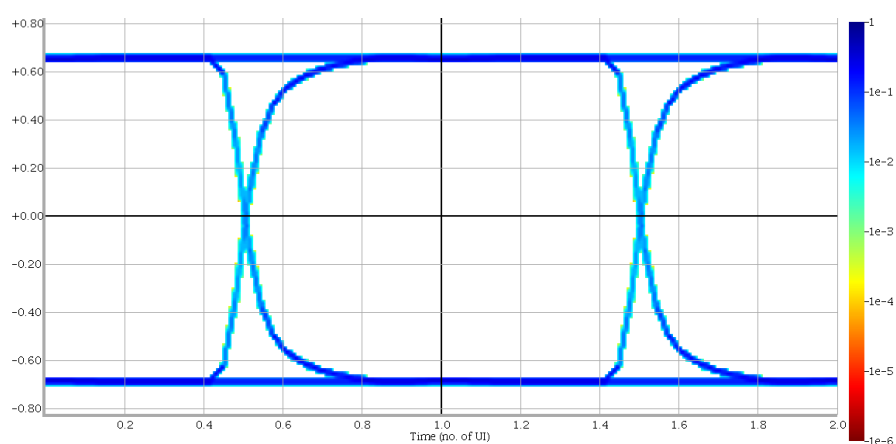
Name: SC_IN_PT1 Height:0.867 Width:0.984 Jitter:0.016



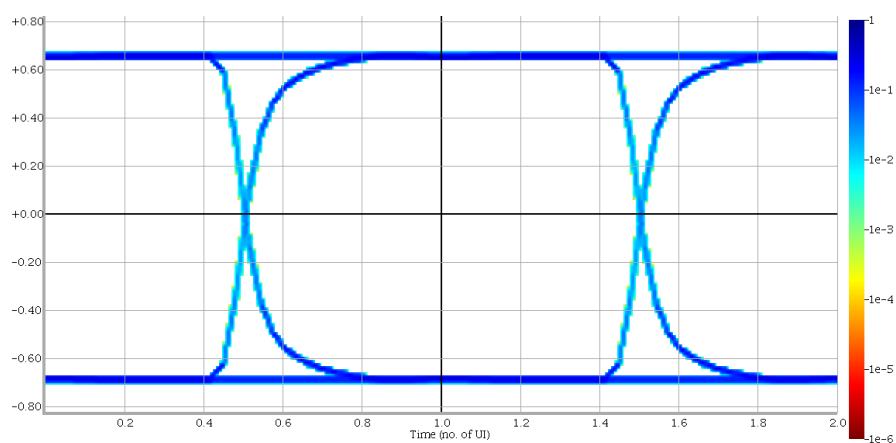
Name: OUT_PT1 Height:0.591 Width:0.984 Jitter:0.016



Name: SC_OUT_PT2 Height:1.449 Width:0.984 Jitter:0.016



Name: RX_IN_PT2 Height:1.321 Width:0.977 Jitter:0.023



Name: OUT_PT2 Height:1.321 Width:0.977 Jitter:0.023

Design Assistance

SCPartname Production folder: <http://www.ti.com/product/DS125BR800>

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