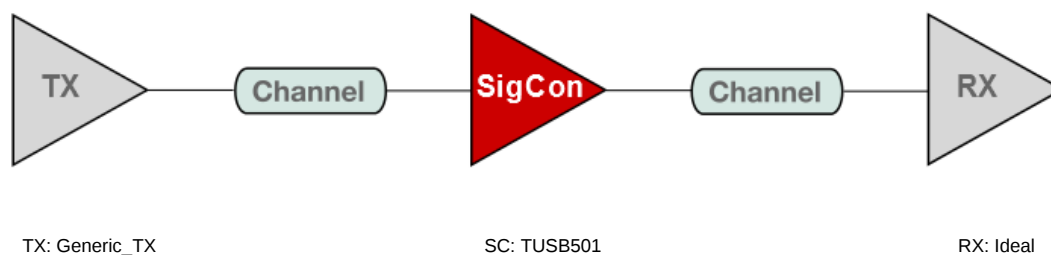


WEBENCH[®] Interface Report

Design : 1194016/2203

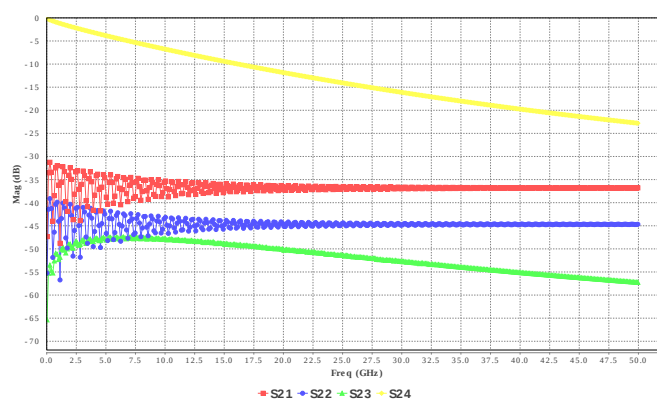
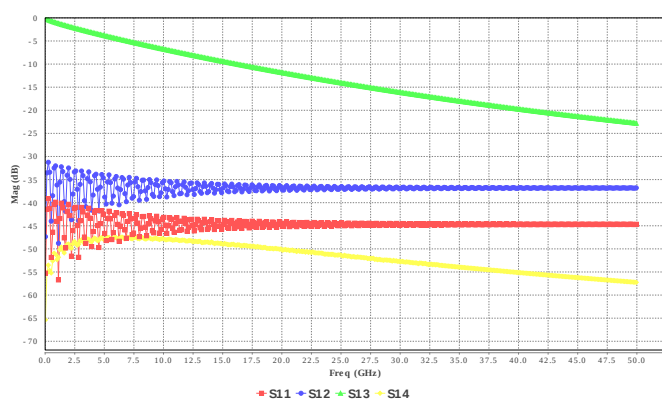
System Block Diagram

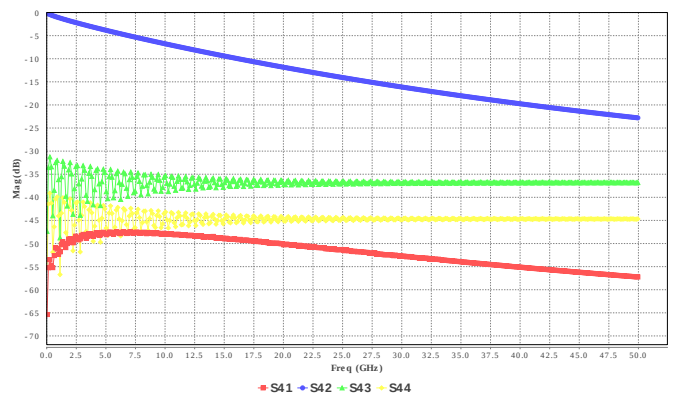
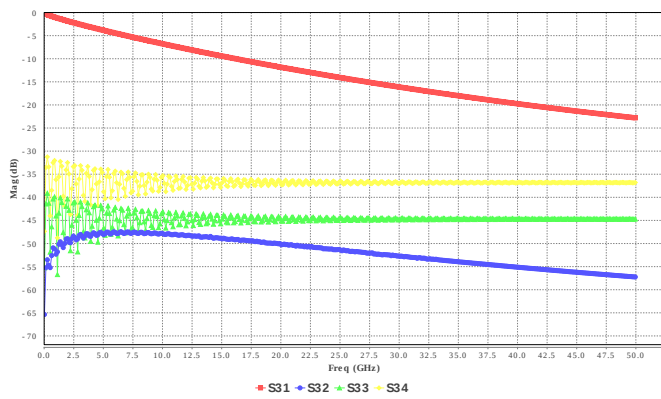


Transmission Medium

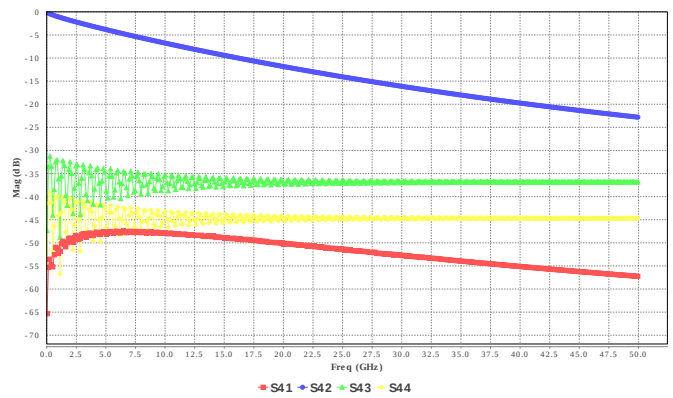
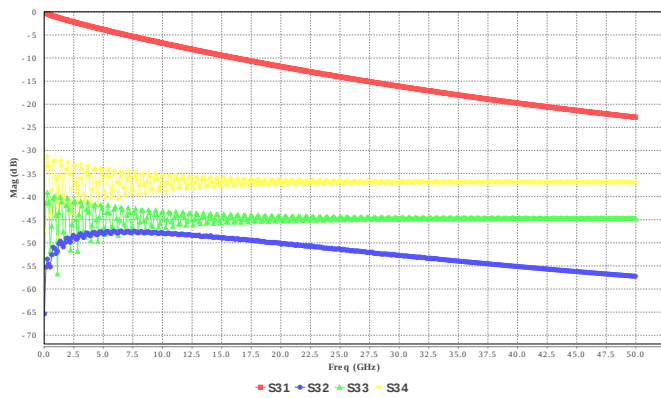
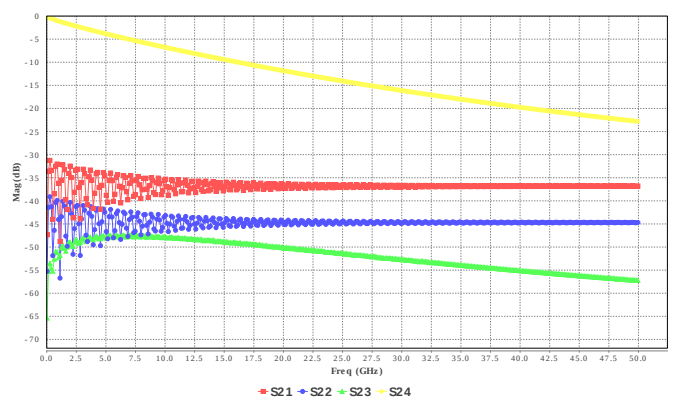
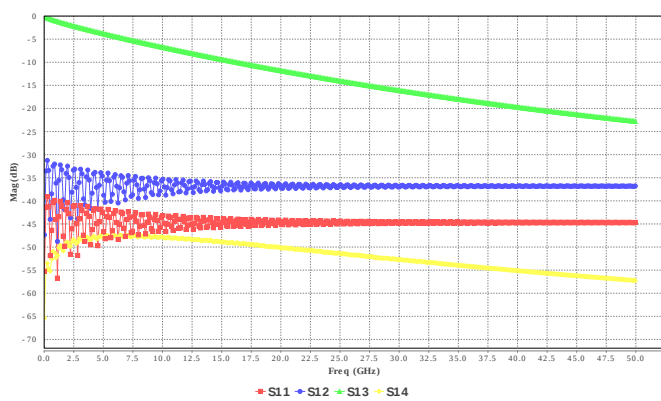


Channel1





Channel2



Simulation Settings

General Parameters:

Data Rate: 2.0E9 Number of UI: 10000.0
SamplesPerBit: 32 Data Pattern: Squire Wave
Jitter RMS: 0.0 BER Extrapolation: false

TX Parameters

#	Name	Value
1.	SlewTime	10
2.	Pre	0
3.	Pst1	0
4.	Pst2	0
5.	Swing	1000

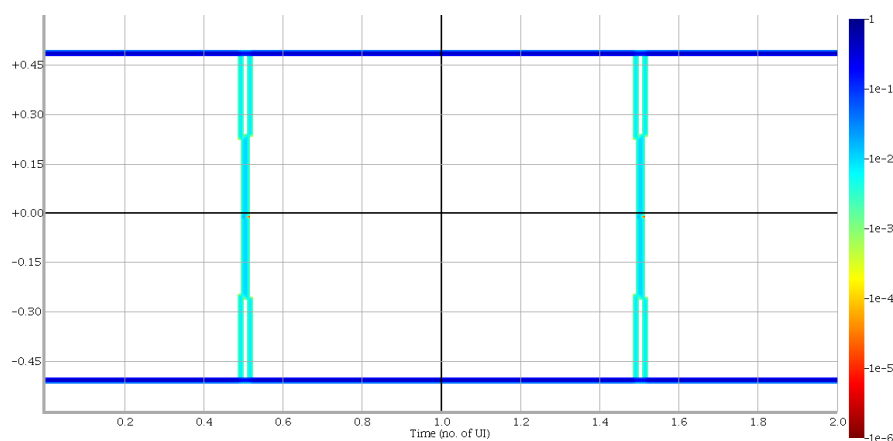
SC RX Parameters

#	Name	Value
1.	EQ	0

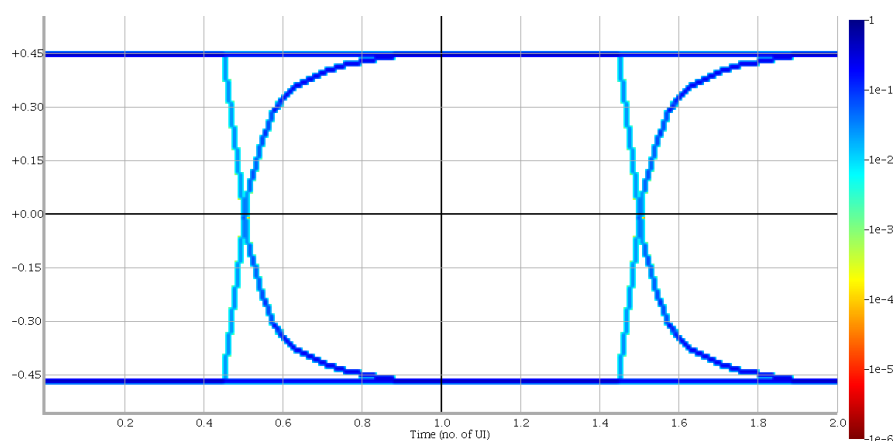
SC TX Parameters

#	Name	Value
1.	OS	0
2.	DE	0

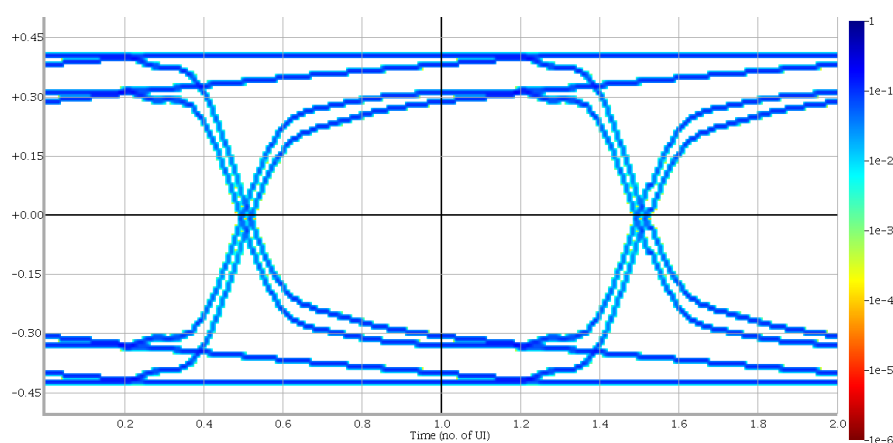
Simulation Results



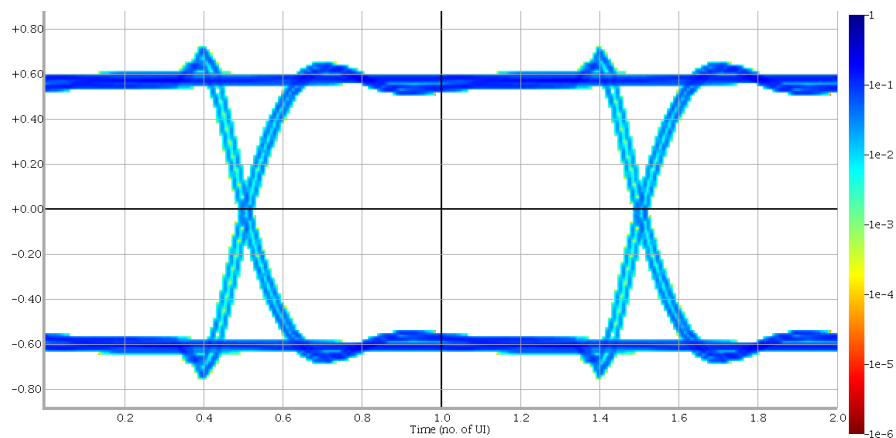
Name: TX_OUT_PT1 Height:0.984 Width:0.984 Jitter:0.016



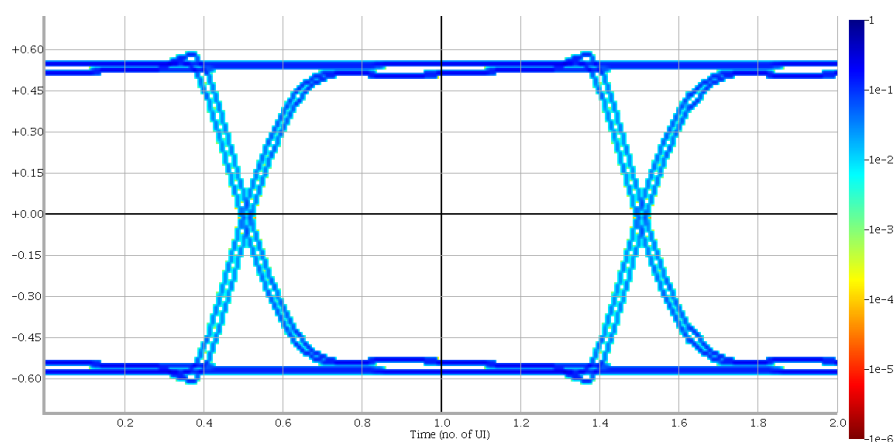
Name: SC_IN_PT1 Height:0.907 Width:0.984 Jitter:0.016



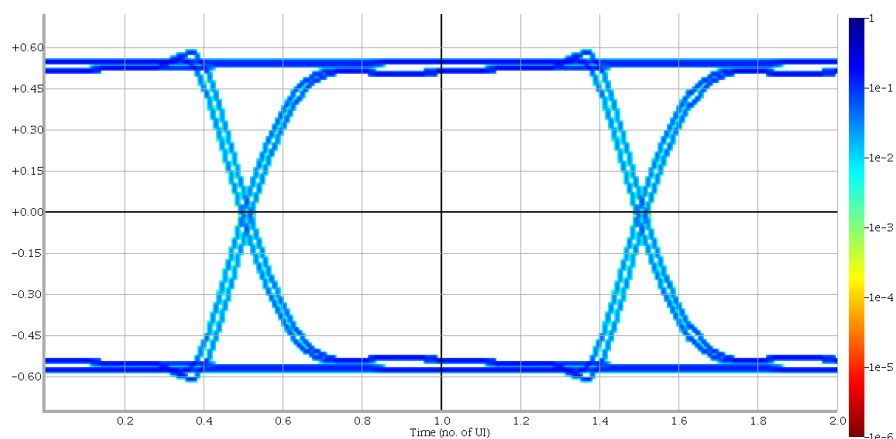
Name: OUT_PT1 Height:0.587 Width:0.938 Jitter:0.062



Name: SC_OUT_PT2 Height:1.082 Width:0.945 Jitter:0.055



Name: RX_IN_PT2 Height:1.044 Width:0.945 Jitter:0.055



Name: OUT_PT2 Height:1.044 Width:0.945 Jitter:0.055

Design Assistance

SCPartname Production folder: <http://www.ti.com/product/TUSB501>

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