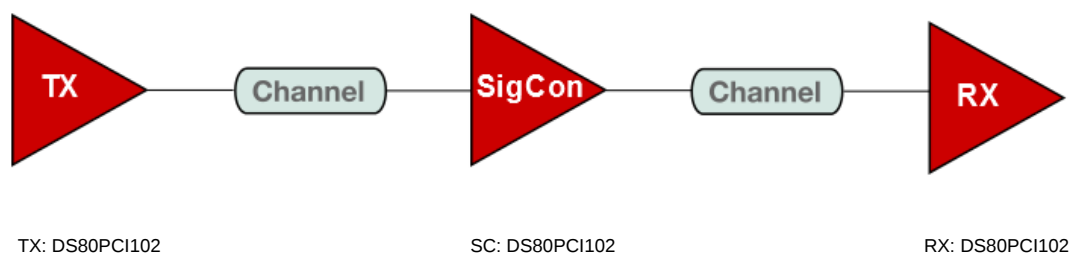


WEBENCH[®] Interface Report

Design : 1194016/2137

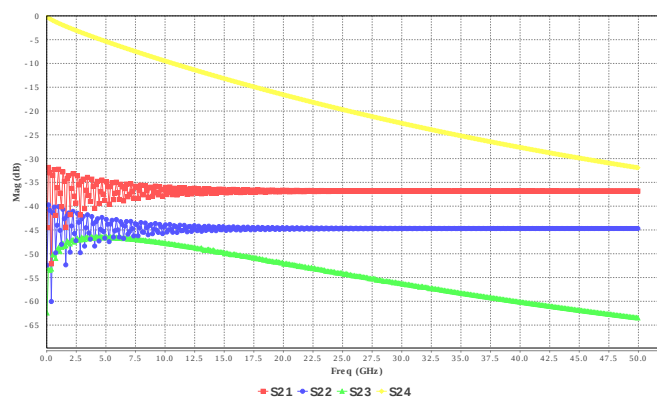
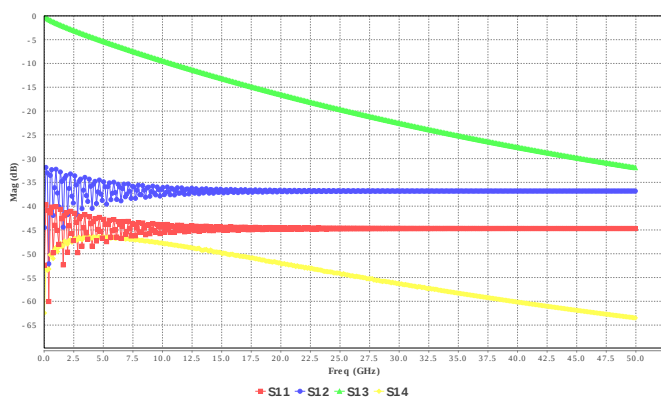
System Block Diagram

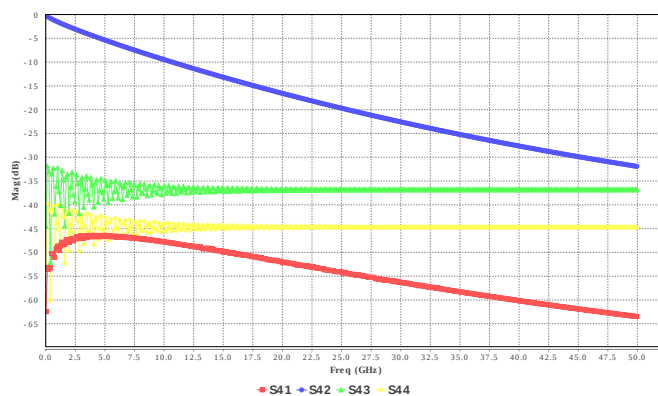
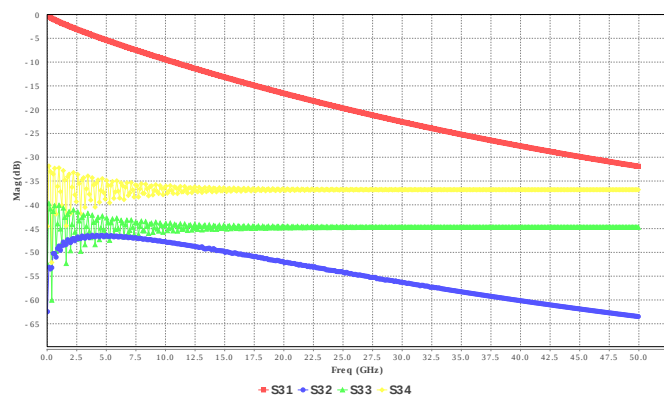


Transmission Medium

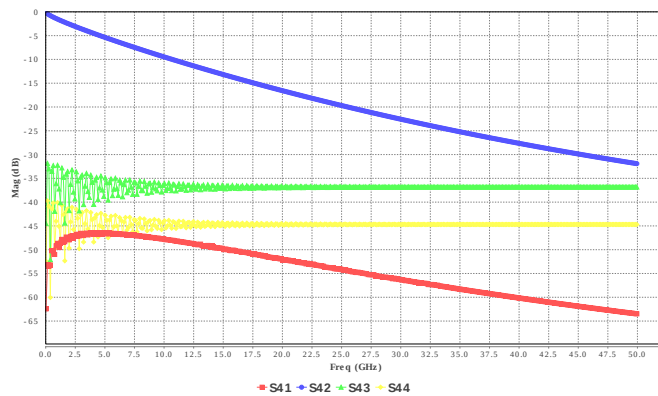
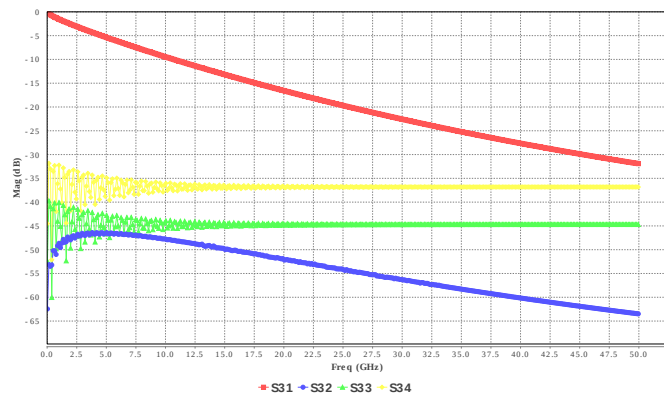
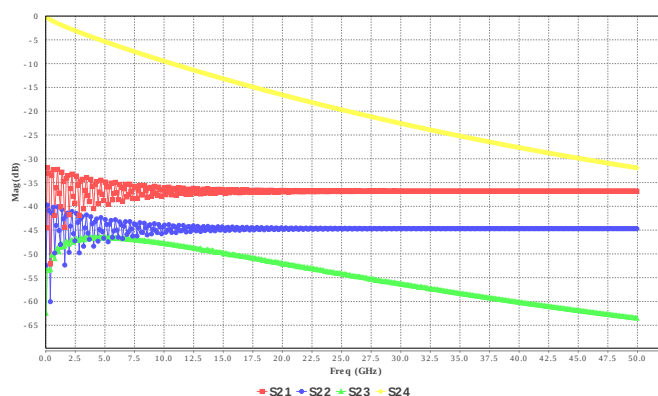
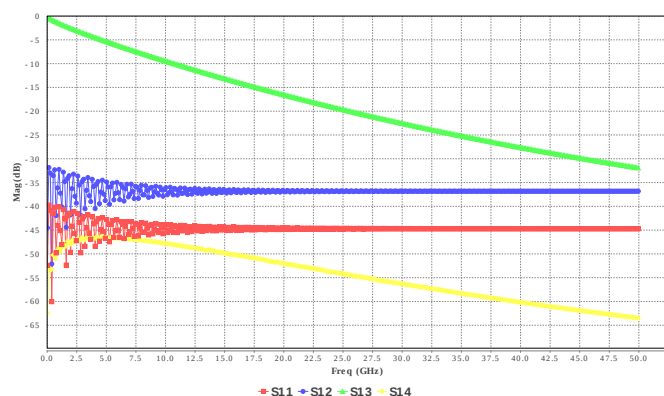


Channel1





Channel2



Simulation Settings

General Parameters:

Data Rate: 1.0E9 Number of UI: 10000.0
 SamplesPerBit: 32 Data Pattern: Squire Wave
 Jitter RMS: 0.0 BER Extrapolation: false

TX Parameters

#	Name	Value
1.	VOD_Level	7
2.	DE_Level	0
3.	gain_debugonly	0.90
4.	LTI_mode	0

SC RX Parameters

#	Name	Value
1.	EQ_Level	0
2.	Limit	1
3.	LTI_mode	0

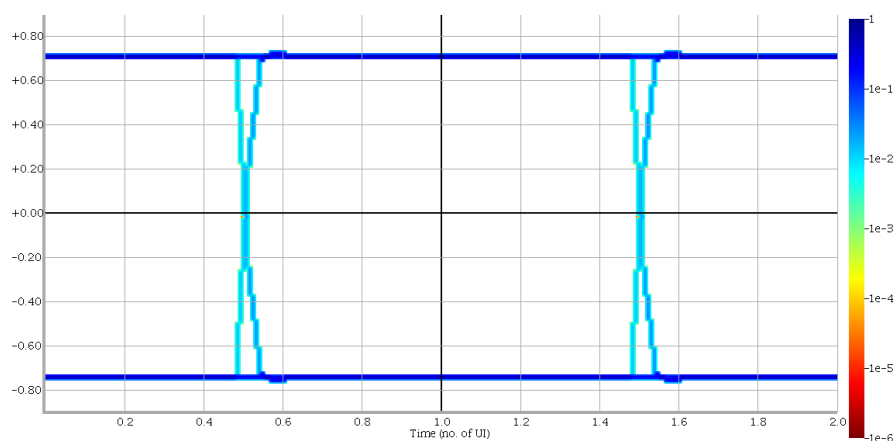
SC TX Parameters

#	Name	Value
1.	VOD_Level	7
2.	DE_Level	0
3.	gain_debugonly	0.90
4.	LTI_mode	0

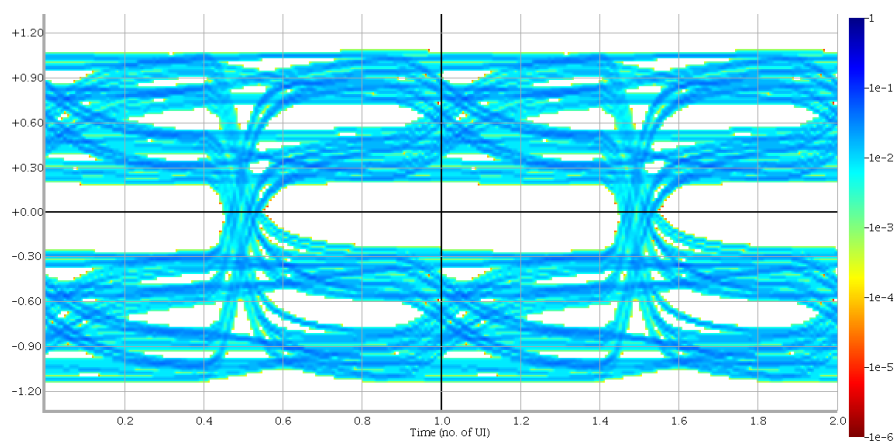
RX Parameters

#	Name	Value
1.	EQ_Level	0
2.	Limit	1
3.	LTI_mode	0

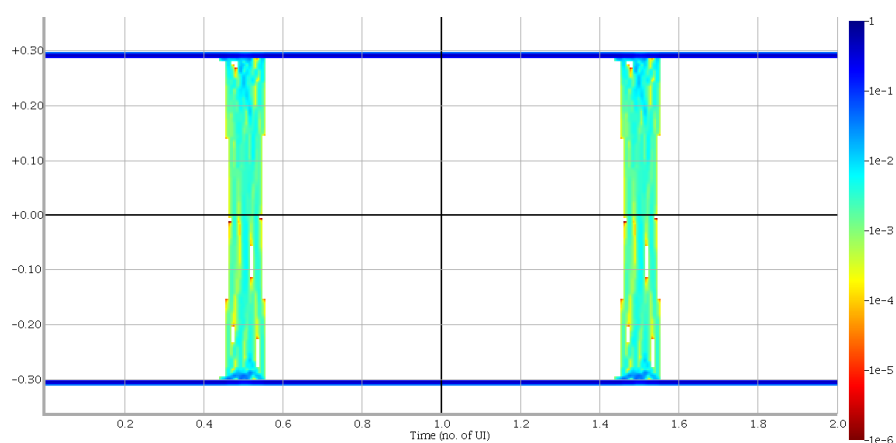
Simulation Results



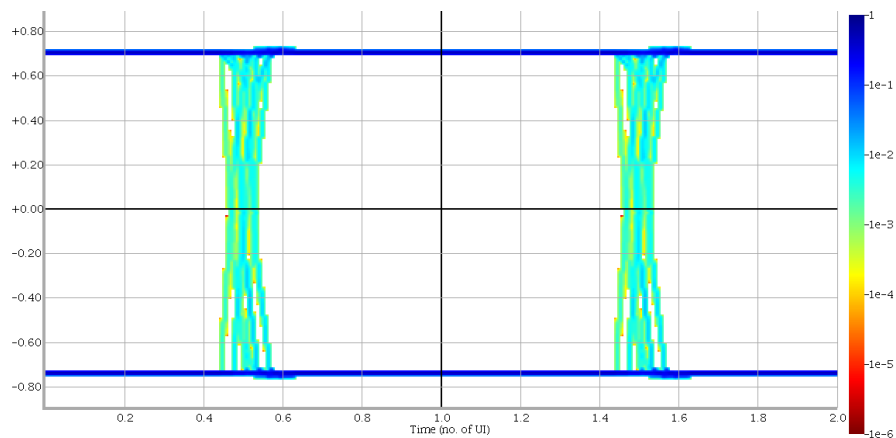
Name: TX_OUT_PT1 Height:1.434 Width:0.984 Jitter:0.016



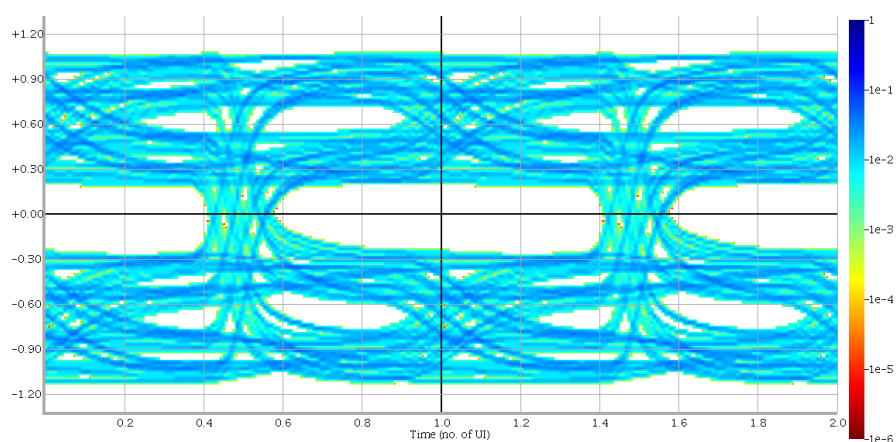
Name: SC_IN_PT1 Height:0.475 Width:0.867 Jitter:0.133



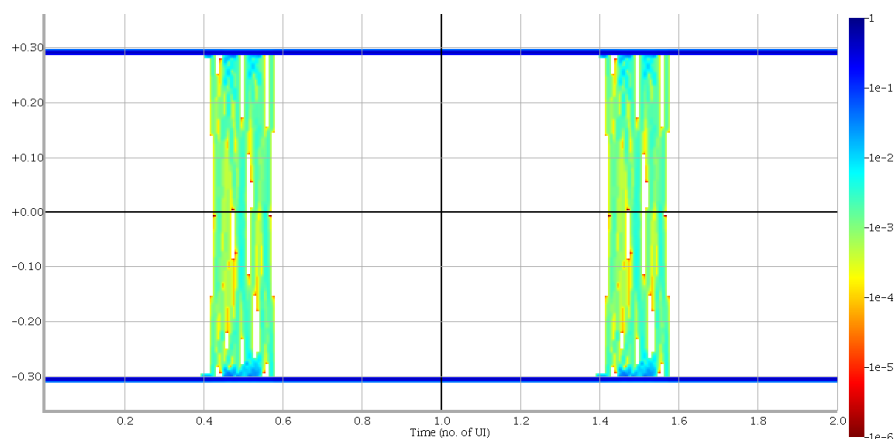
Name: OUT_PT1 Height:0.591 Width:0.922 Jitter:0.078



Name: SC_OUT_PT2 Height:1.429 Width:0.922 Jitter:0.078



Name: RX_IN_PT2 Height:0.453 Width:0.805 Jitter:0.195



Name: OUT_PT2 Height:0.591 Width:0.859 Jitter:0.141

Design Assistance

TXPartname Production folder: <http://www.ti.com/product/DS80PC1102>

SCPartname Production folder: <http://www.ti.com/product/DS80PC1102>

RXPartname Product folder: <http://www.ti.com/product/DS80PC1102>

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