

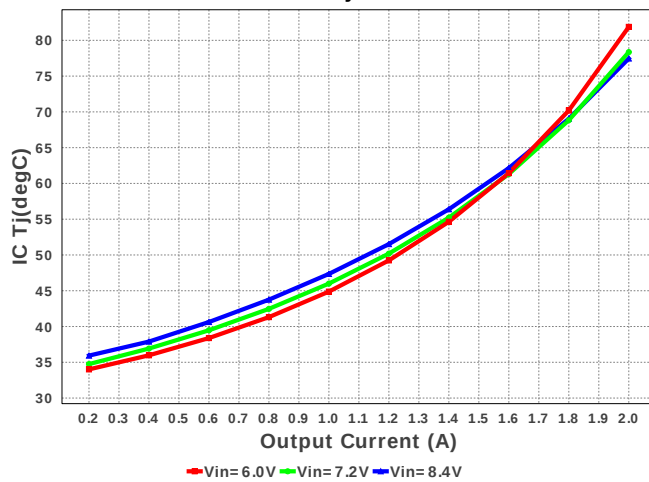


Device = TPS54232DR
Topology = Buck
Created = 7/17/16 11:02:03 PM
BOM Cost = \$1.15
BOM Count = 14
Total Pd = 1.03W

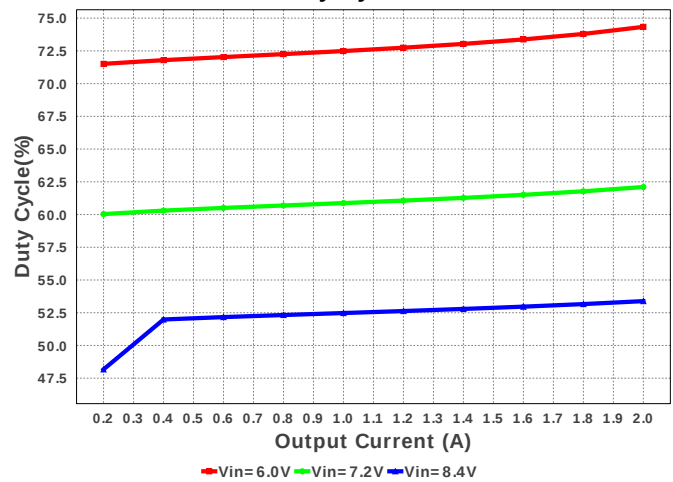
#	Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
12.	Rfbb	Yageo America	RC0603FR-072K4L Series= ?	Res= 2.4 kOhm Power= 100.0 mW Tolerance= 1.0%	1	\$0.01	0603 5 mm ²
13.	Rfbt	Vishay-Dale	CRCW040210K2FKED Series= CRCW..e3	Res= 10.2 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
14.	U1	Texas Instruments	TPS54232DR	Switcher	1	\$0.55	

D0008A 57 mm²

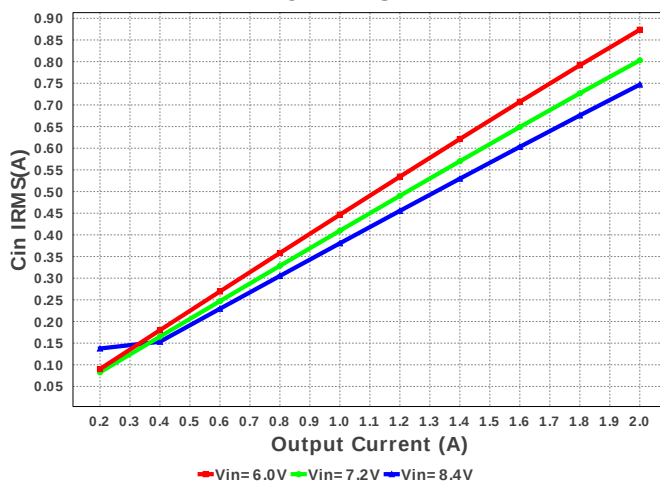
IC Tj



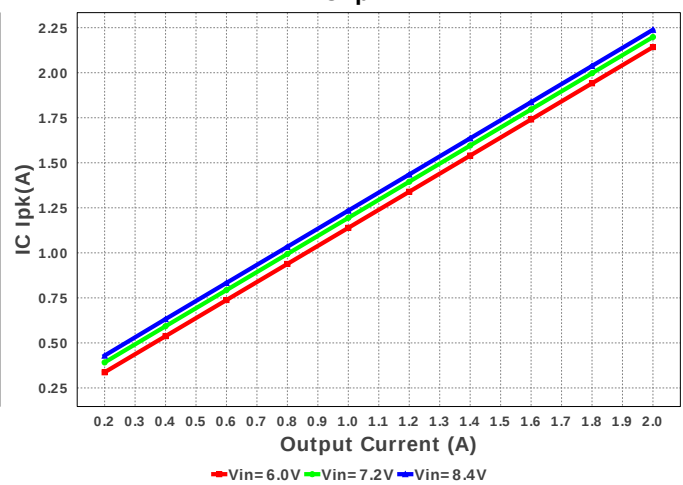
Duty Cycle

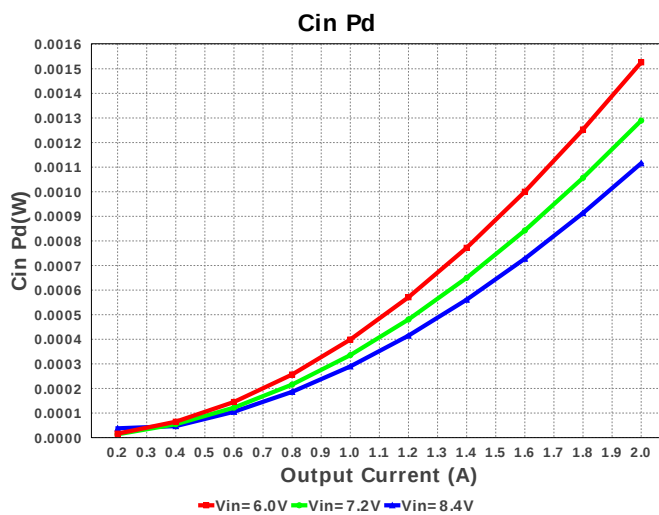
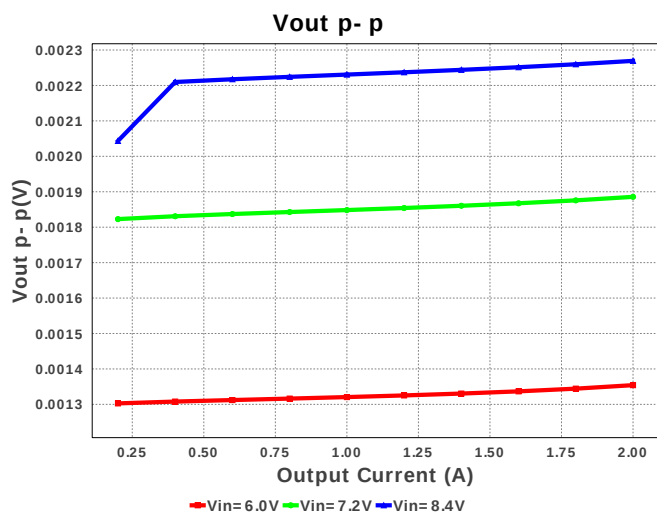
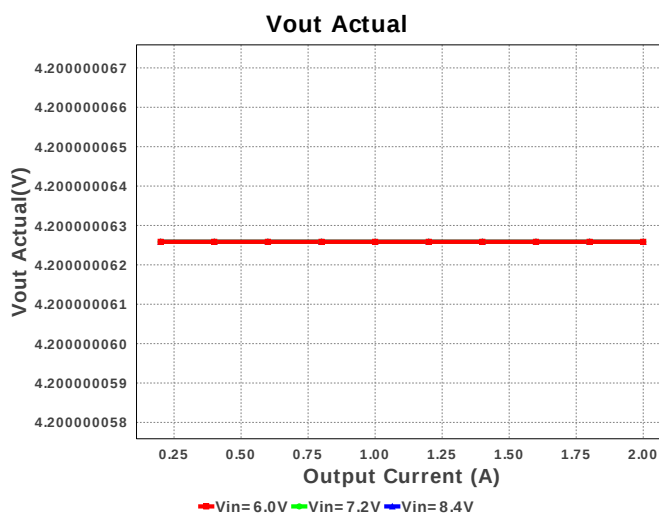
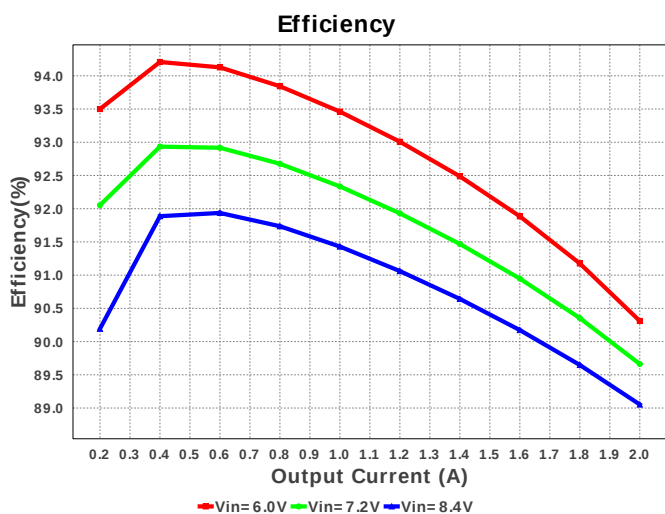
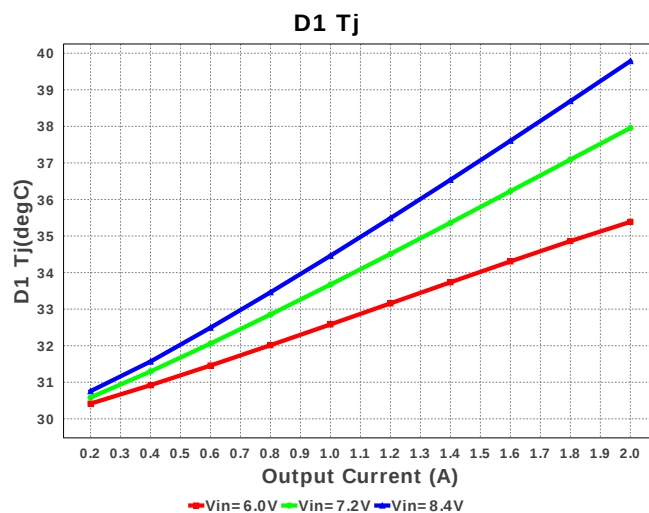
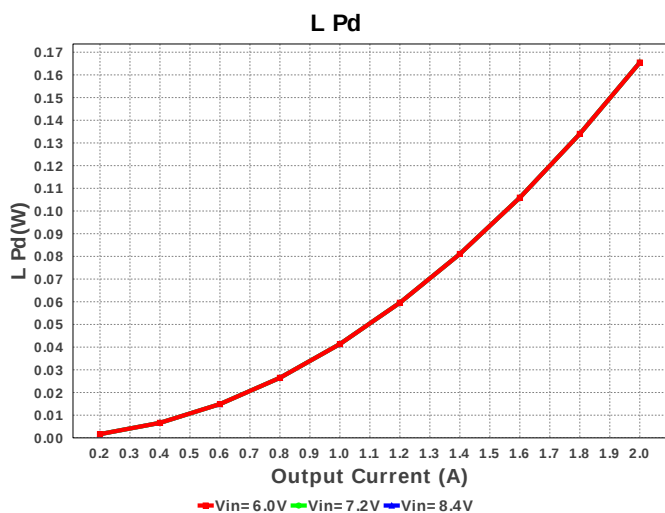


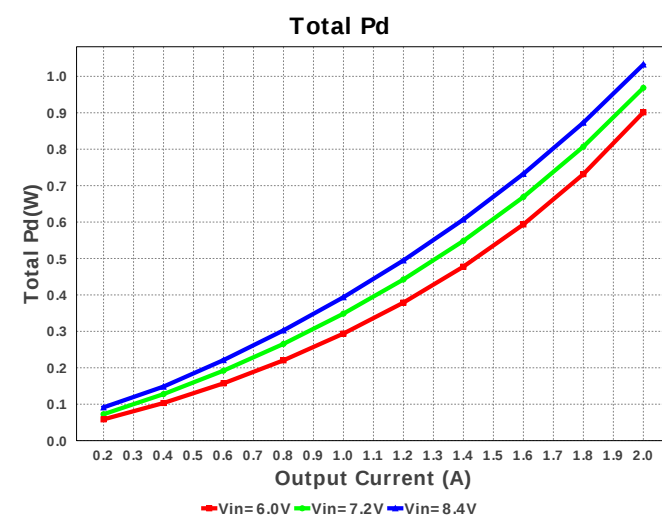
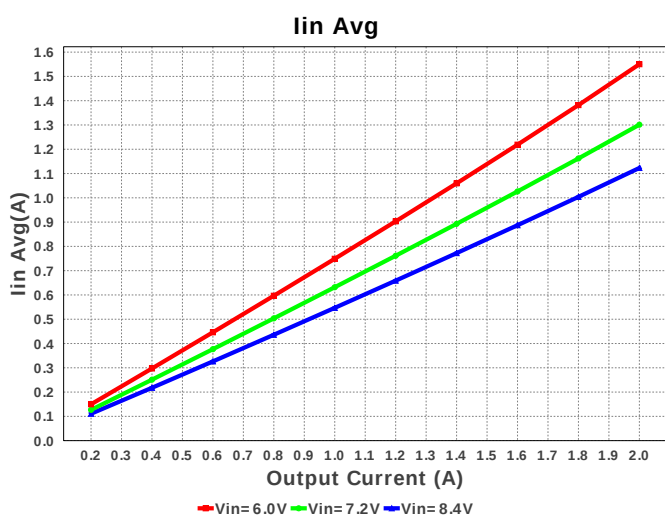
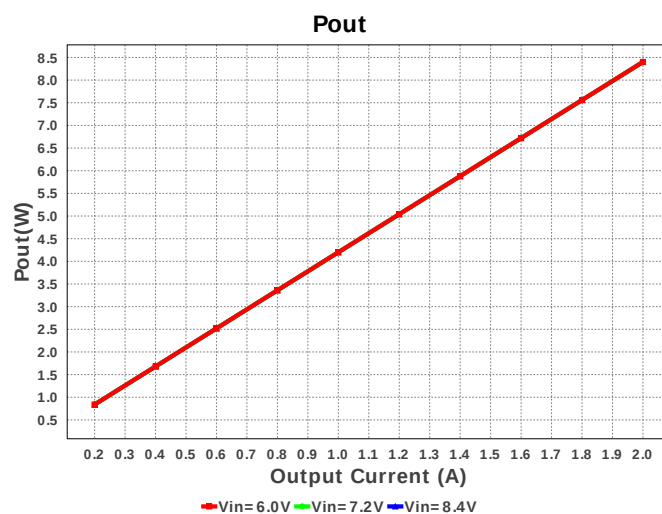
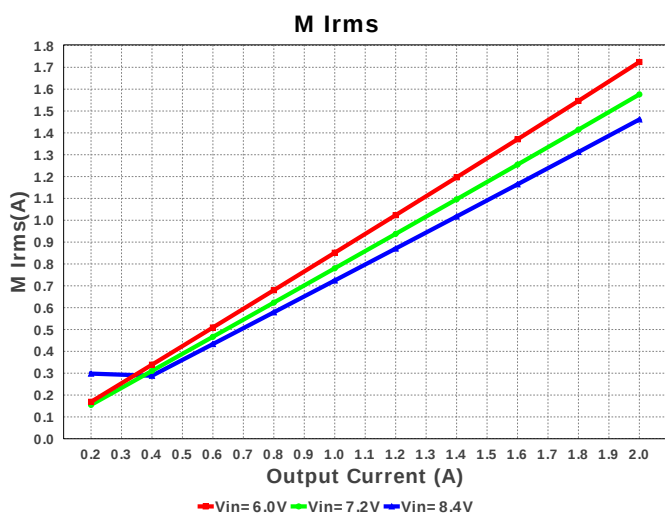
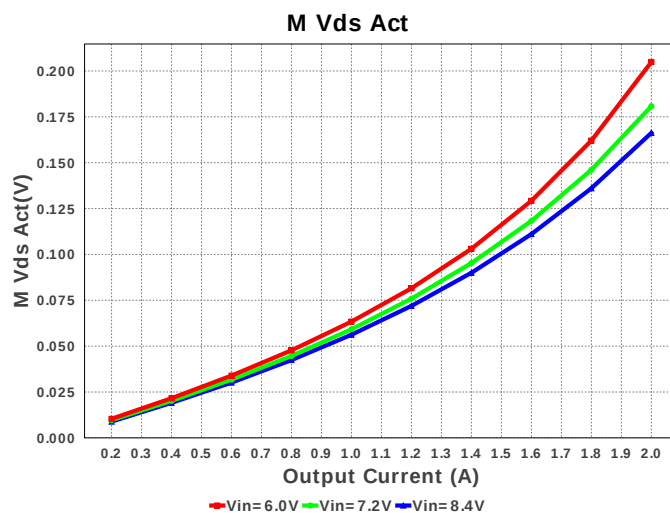
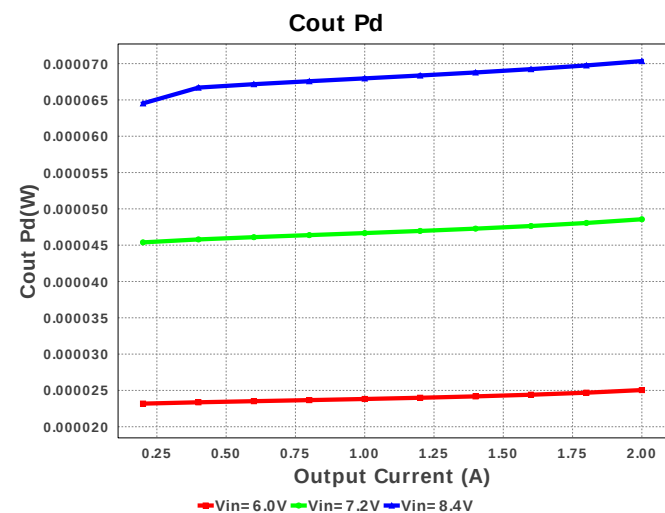
Cin IRMS

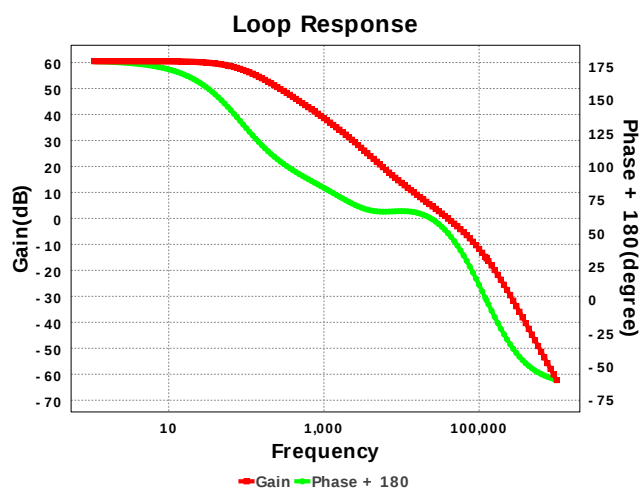
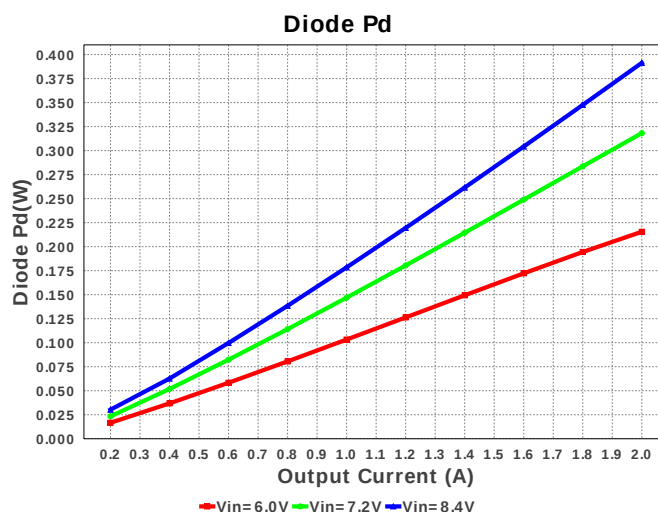
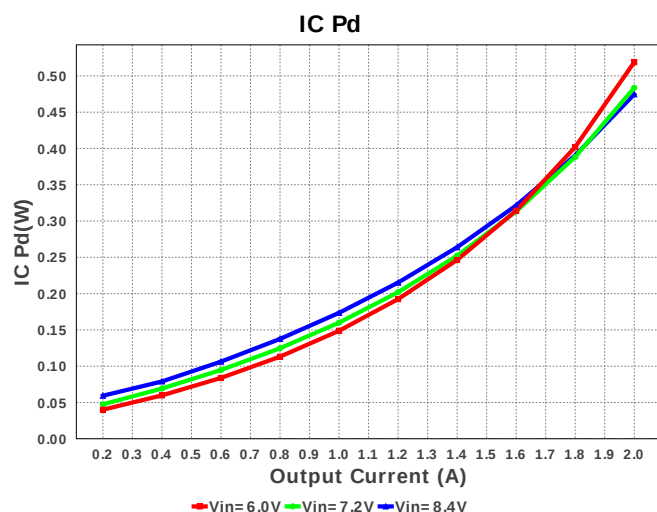
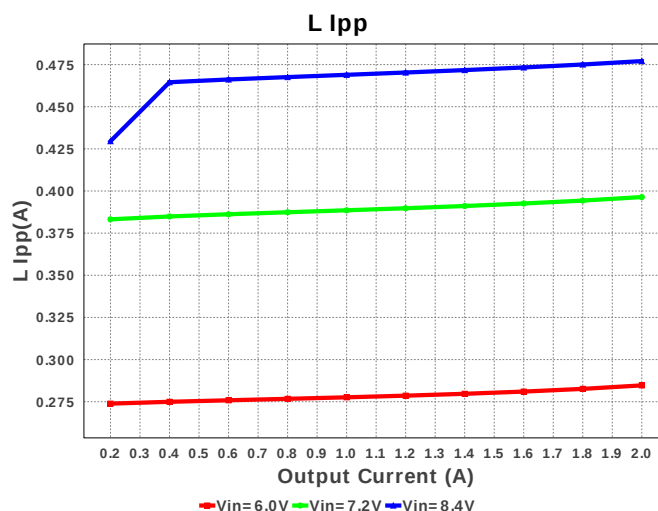
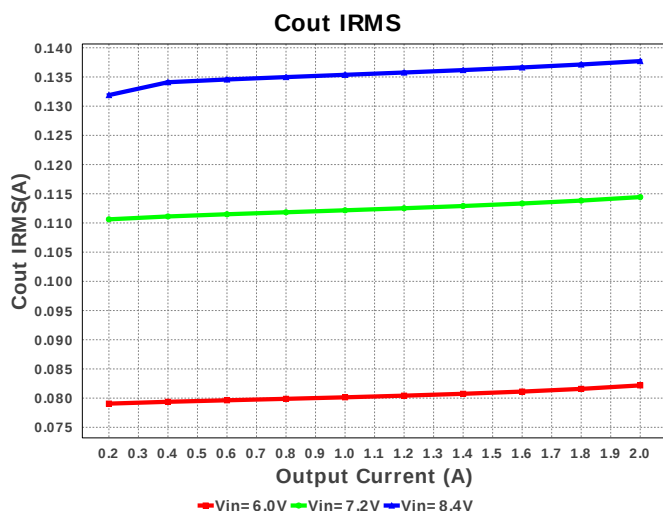


IC Ipk









Operating Values

#	Name	Value	Category	Description
1.	Cin IRMS	746.921 mA	Current	Input capacitor RMS ripple current
2.	Cout IRMS	137.718 mA	Current	Output capacitor RMS ripple current
3.	IC Ipk	2.239 A	Current	Peak switch current in IC
4.	Iin Avg	1.123 A	Current	Average input current
5.	L Ipp	477.07 mA	Current	Peak-to-peak inductor ripple current
6.	M1 Irms	1.461 A	Current	Q lavg
7.	BOM Count	14	General	Total Design BOM count
8.	FootPrint	226.0 mm ²	General	Total Foot Print Area of BOM components
9.	Frequency	1000.0 kHz	General	Switching frequency
10.	M Vds Act	166.236 mV	General	Voltage drop across the MosFET
11.	Pout	8.4 W	General	Total output power

#	Name	Value	Category	Description
12.	Total BOM	\$1.15	General	Total BOM Cost
13.	D1 Tj	39.784 degC	Op_Point	D1 junction temperature
14.	Low Freq Gain	60.511 dB	Op_Point	Gain at 10Hz
15.	Vout Actual	4.2 V	Op_Point	Vout Actual calculated based on selected voltage divider resistors
16.	Vout OP	4.2 V	Op_Point	Operational Output Voltage
17.	Cross Freq	39.703 kHz	Op_point	Bode plot crossover frequency
18.	Duty Cycle	53.386 %	Op_point	Duty cycle
19.	Efficiency	89.053 %	Op_point	Steady state efficiency
20.	Gain Marg	-15.401 dB	Op_point	Bode Plot Gain Margin
21.	IC Tj	77.46 degC	Op_point	IC junction temperature
22.	ICThetaJA	100.0 degC/W	Op_point	IC junction-to-ambient thermal resistance
23.	IOUT_OP	2.0 A	Op_point	Iout operating point
24.	Phase Marg	50.284 deg	Op_point	Bode Plot Phase Margin
25.	VIN_OP	8.4 V	Op_point	Vin operating point
26.	Vout p-p	2.269 mV	Op_point	Peak-to-peak output ripple voltage
27.	Cin Pd	1.116 mW	Power	Input capacitor power dissipation
28.	Cout Pd	70.345 μ W	Power	Output capacitor power dissipation
29.	Diode Pd	391.377 mW	Power	Diode power dissipation
30.	IC Pd	474.596 mW	Power	IC power dissipation
31.	L Pd	165.44 mW	Power	Inductor power dissipation
32.	Total Pd	1.033 W	Power	Total Power Dissipation
33.	Vout Tolerance	5.193 %		Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable

Design Inputs

#	Name	Value	Description
1.	Iout	2.0	Maximum Output Current
2.	VinMax	8.4	Maximum input voltage
3.	VinMin	6.0	Minimum input voltage
4.	Vout	4.2	Output Voltage
5.	base_pn	TPS54232	Base Product Number
6.	source	DC	Input Source Type
7.	Ta	30.0	Ambient temperature

Design Assistance

1. **TPS54232** Product Folder : <http://www.ti.com/product/TPS54232> : contains the data sheet and other resources.

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