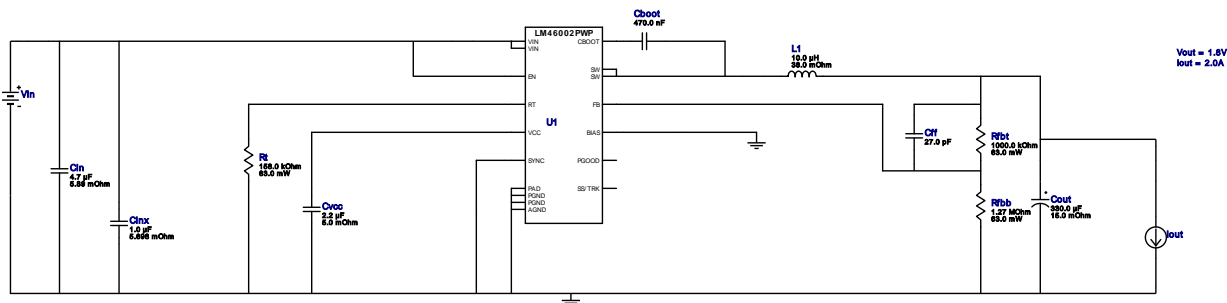





## WEBENCH® Design Report

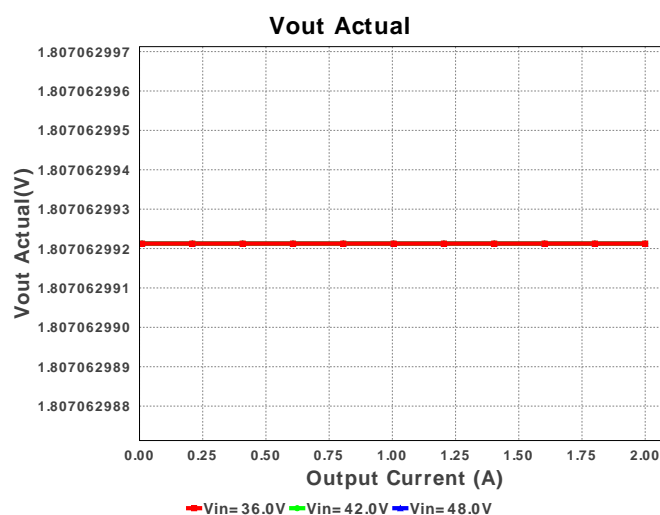
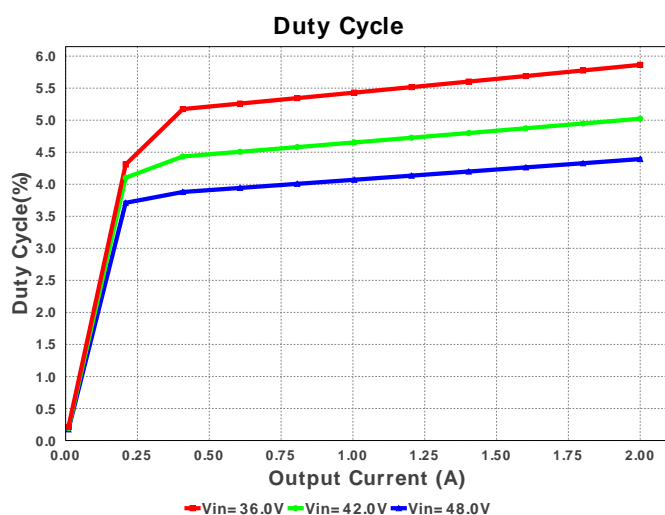
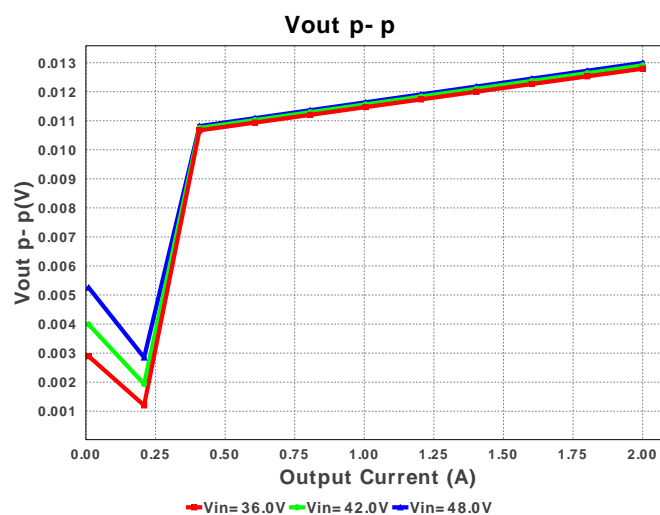
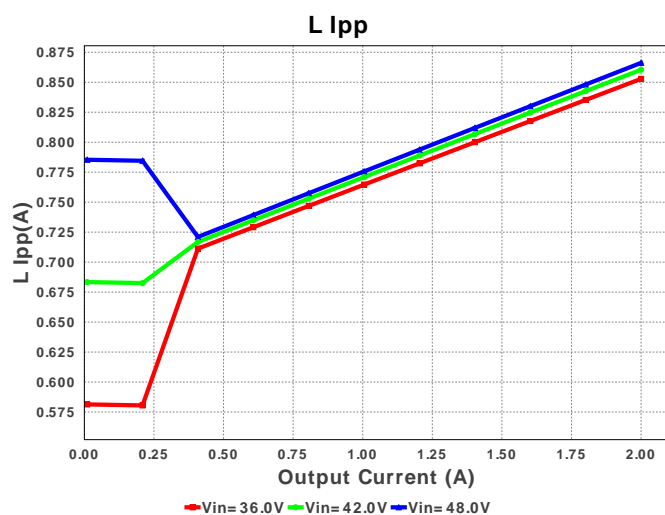
Design : 4466246/47 LM46002PWPR  
LM46002PWPR 36.0V-48.0V to 1.80V @ 2.0A


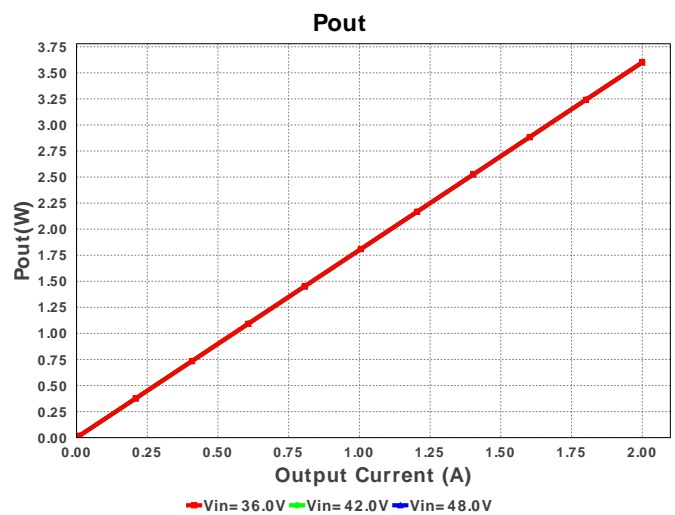
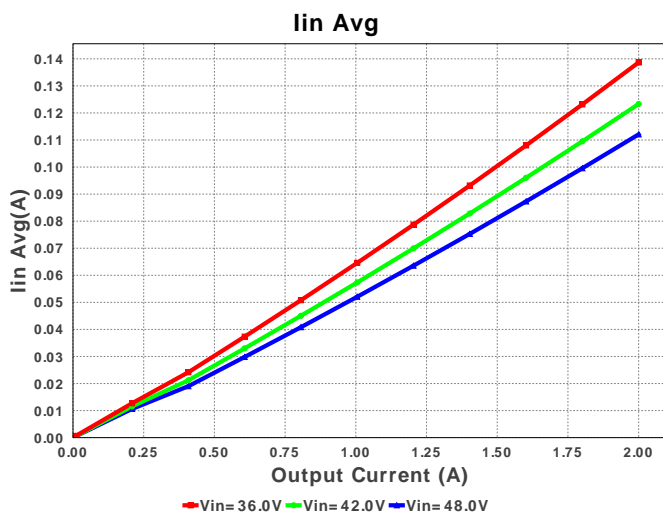
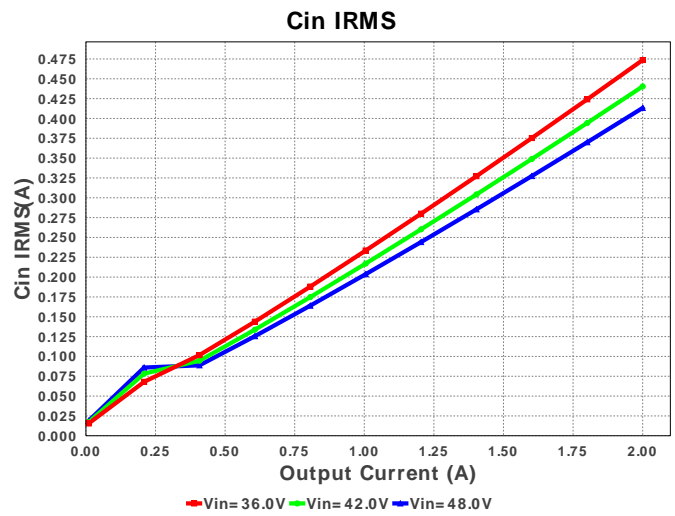
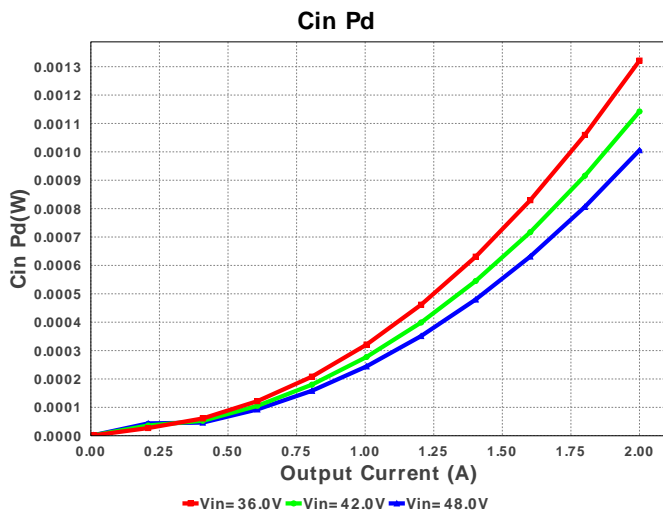
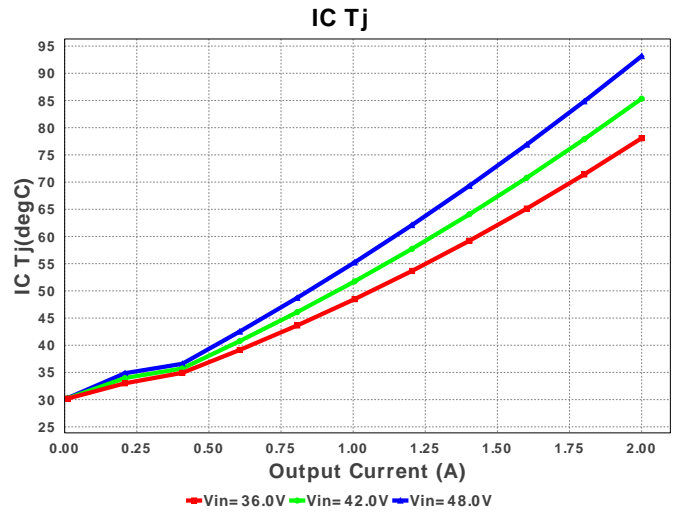
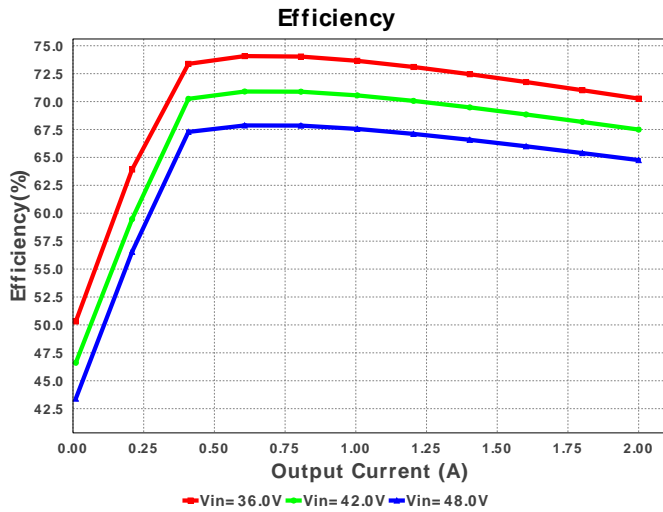
1. The input capacitor included in the BOM only contains a small filter capacitor that should be placed near the IC. Depending on where the power supply is laid out in the system additional bulk capacitance may need to be added to filter the line ripple.
2. If there is no VinTyp specified, WEBENCH will use the VinMax value. To change the VinTyp value, click on the "Change Design Inputs" button under the Optimization Tuning knob. In some applications, while the design requires the input voltage to be a wide range, for a majority of the time, it is operating at a much lower voltage than the maximum input voltage. Sizing the inductor based on the maximum input voltage may yield an inductance much larger than typically needed, causing a larger footprint for the overall design. At the same time, components such as the input capacitor must be rated based on the maximum input voltage. WEBENCH now supports the use of this additional input voltage specification.

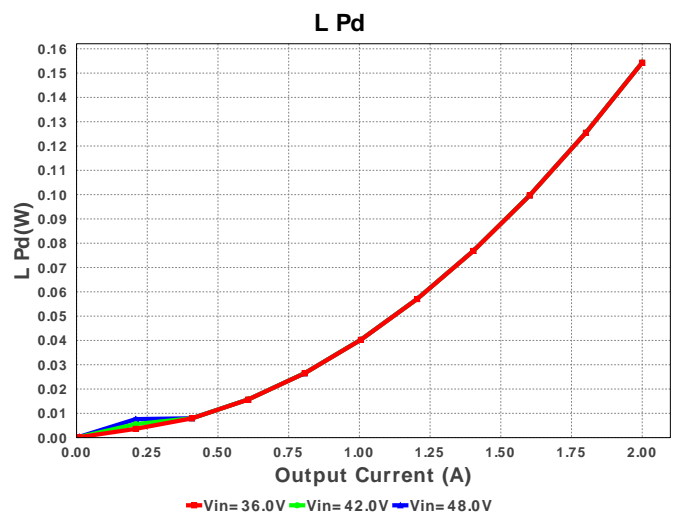
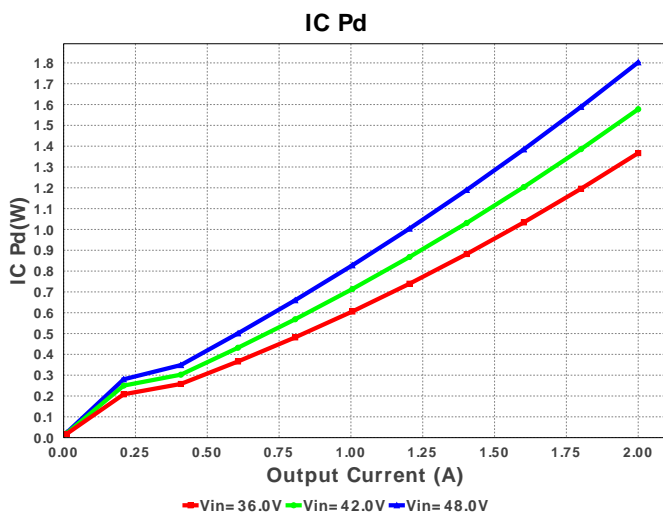
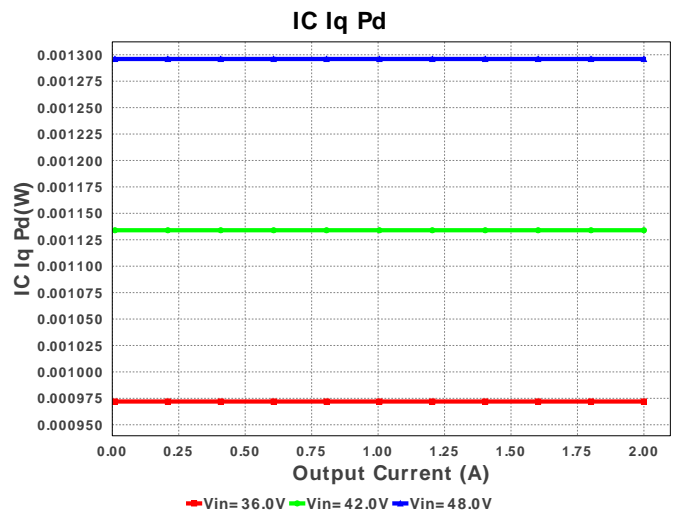
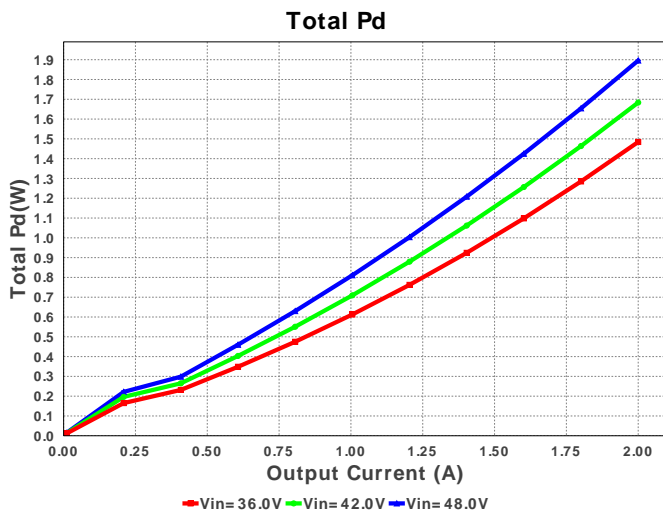
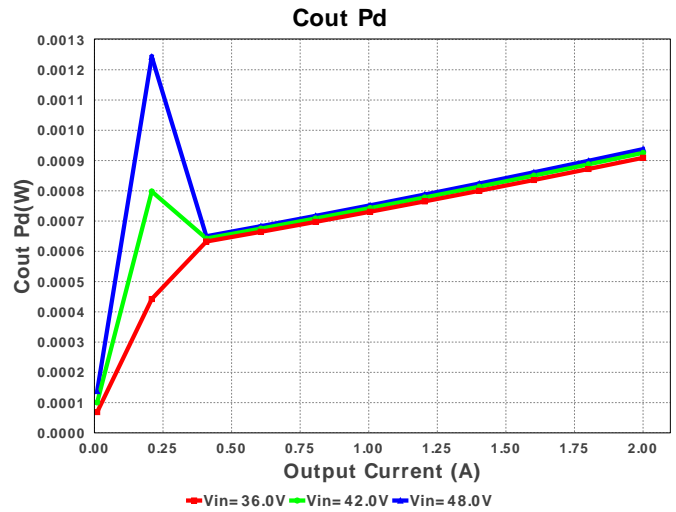
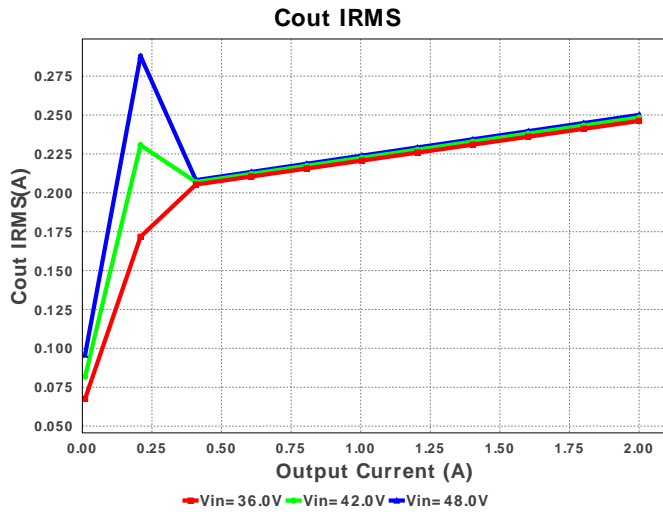
## Electrical BOM

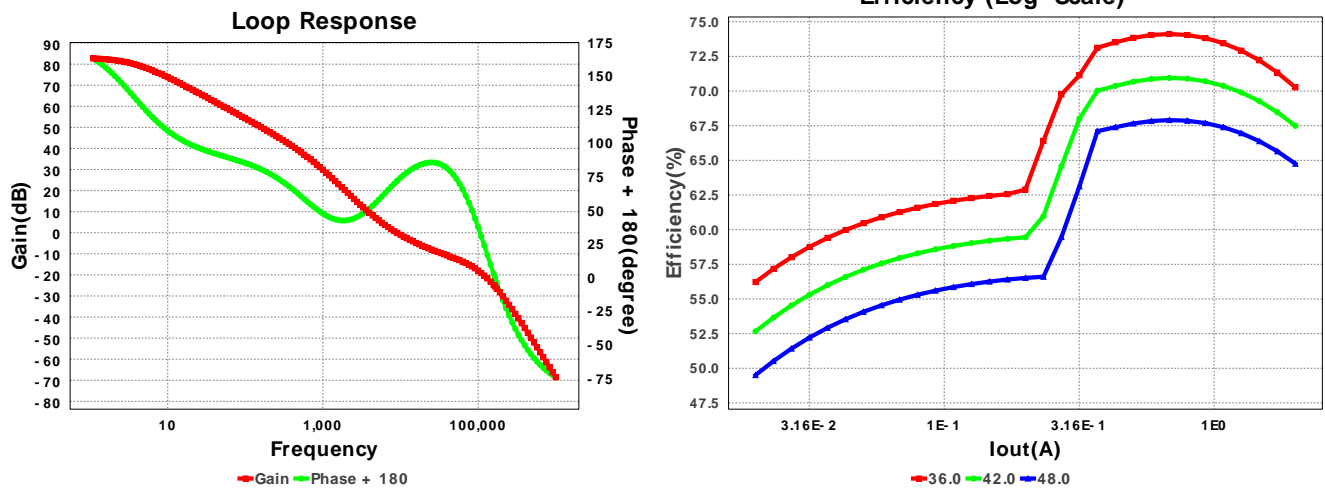
#	Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
1.	Cboot	MuRata	GRM155C80J474KE19D Series= X6S	Cap= 470.0 nF VDC= 6.3 V IRMS= 0.0 A	1	\$0.01	 0402 3 mm <sup>2</sup>
2.	Cff	Kemet	C0201C270J3GACTU Series= C0G/NP0	Cap= 27.0 pF VDC= 5.0 V IRMS= 0.0 A	1	\$0.01	 0201 2 mm <sup>2</sup>
3.	Cin	TDK	C3225X7S2A475M200AB Series= X7S	Cap= 4.7 uF ESR= 5.89 mOhm VDC= 100.0 V IRMS= 6.7739 A	1	\$0.42	 1210 15 mm <sup>2</sup>
4.	Cinx	TDK	C3216X5R2A105K Series= X5R	Cap= 1.0 uF ESR= 5.698 mOhm VDC= 100.0 V IRMS= 0.0 A	1	\$0.07	 1206 11 mm <sup>2</sup>
5.	Cout	Panasonic	4SVPC330MV Series= SVPC	Cap= 330.0 uF ESR= 15.0 mOhm VDC= 4.0 V IRMS= 3.16 A	1	\$0.32	 SM_RADIAL_6.3AMM 80 mm <sup>2</sup>
6.	Cvcc	MuRata	GRM155R60J225ME15D Series= X5R	Cap= 2.2 uF ESR= 5.0 mOhm VDC= 6.3 V IRMS= 3.67 A	1	\$0.02	 0402 3 mm <sup>2</sup>
7.	L1	TDK	VLP8040T-100M	L= 10.0 uH DCR= 38.0 mOhm	1	\$0.22	 VLP8040 113 mm <sup>2</sup>
8.	Rfb1	Vishay-Dale	CRCW04021M27FKED Series= CRCW..e3	Res= 1.27 MOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm <sup>2</sup>

#	Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
9.	Rfbt	Vishay-Dale	CRCW04021M00FKED Series= CRCW..e3	Res= 1000.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm <sup>2</sup>
10.	Rt	Vishay-Dale	CRCW0402158KFKED Series= CRCW..e3	Res= 158.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm <sup>2</sup>
11.	U1	Texas Instruments	LM46002PWPR	Switcher	1	\$1.85	 PWP0016F 59 mm <sup>2</sup>









## Operating Values

#	Name	Value	Category	Description
1.	Cin IRMS	413.169 mA	Current	Input capacitor RMS ripple current
2.	Cout IRMS	247.321 mA	Current	Output capacitor RMS ripple current
3.	Iin Avg	112.38 mA	Current	Average input current
4.	L Ipp	856.75 mA	Current	Peak-to-peak inductor ripple current
5.	BOM Count	11	General	Total Design BOM count
6.	FootPrint	295.0 mm <sup>2</sup>	General	Total Foot Print Area of BOM components
7.	Frequency	256.25 kHz	General	Switching frequency
8.	Pout	3.6 W	General	Total output power
9.	Total BOM	\$2.95	General	Total BOM Cost
10.	Low Freq Gain	82.745 dB	Op_Point	Gain at 10Hz
11.	Vout Actual	1.807 V	Op_Point	Vout Actual calculated based on selected voltage divider resistors
12.	Vout OP	1.8 V	Op_Point	Operational Output Voltage
13.	Cross Freq	9.002 kHz	Op_point	Bode plot crossover frequency
14.	Duty Cycle	4.394 %	Op_point	Duty cycle
15.	Efficiency	64.571 %	Op_point	Steady state efficiency
16.	Gain Marg	-25.561 dB	Op_point	Bode Plot Gain Margin
17.	IC Tj	93.719 degC	Op_point	IC junction temperature
18.	ICThetaJA	38.9 degC/W	Op_point	IC junction-to-ambient thermal resistance
19.	IOUT_OP	2.0 A	Op_point	Iout operating point
20.	Phase Marg	72.113 deg	Op_point	Bode Plot Phase Margin
21.	VIN_OP	48.0 V	Op_point	Vin operating point
22.	Vout p-p	12.851 mV	Op_point	Peak-to-peak output ripple voltage
23.	Cin Pd	1.005 mW	Power	Input capacitor power dissipation
24.	Cout Pd	917.516 μW	Power	Output capacitor power dissipation
25.	IC Iq Pd	1.296 mW	Power	IC Iq Pd
26.	IC Pd	1.819 W	Power	IC power dissipation
27.	L Pd	154.324 mW	Power	Inductor power dissipation
28.	Total Pd	1.911 W	Power	Total Power Dissipation
29.	Vout Tolerance	1.888 %		Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable

## Design Inputs

#	Name	Value	Description
1.	Iout	2.0	Maximum Output Current
2.	VinMax	48.0	Maximum input voltage
3.	VinMin	36.0	Minimum input voltage
4.	Vout	1.8	Output Voltage
5.	base_pn	LM46002	Base Product Number
6.	source	DC	Input Source Type
7.	Ta	30.0	Ambient temperature

## Design Assistance

1. **LM46002** Product Folder : <http://www.ti.com/product/LM46002> : contains the data sheet and other resources.

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**You should completely validate and test your design implementation to confirm the system functionality for your application prior to production.**

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