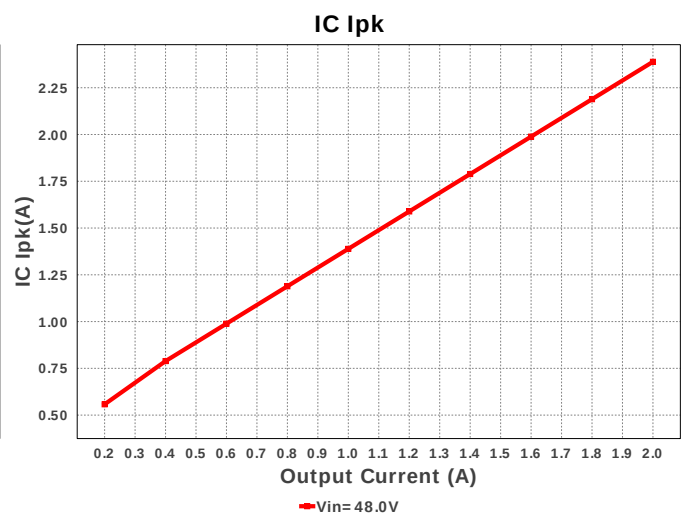
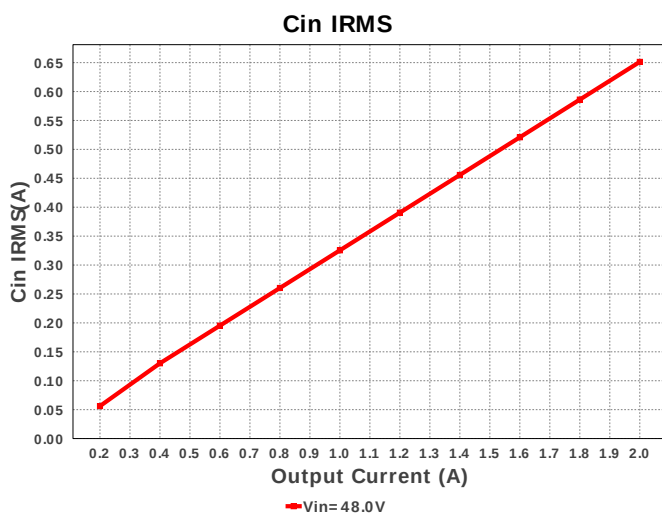
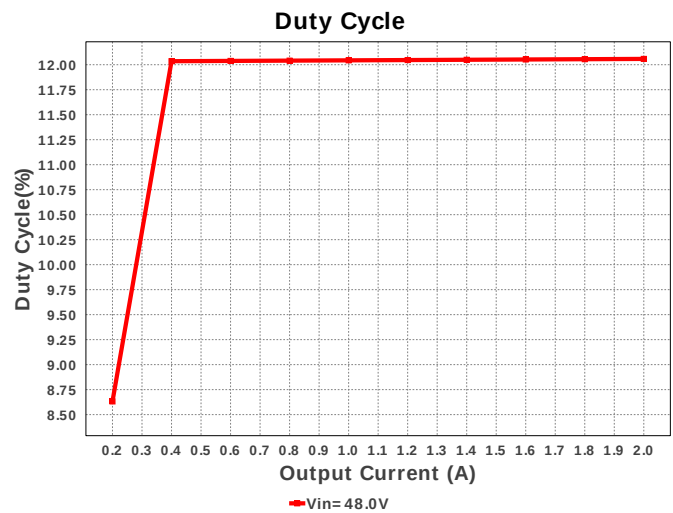
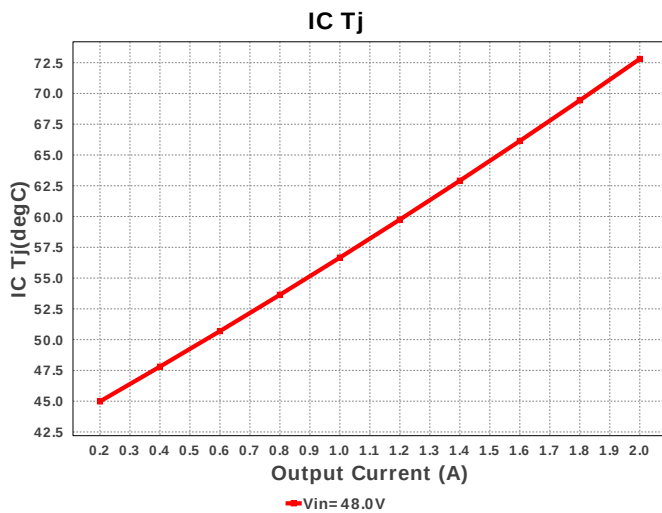
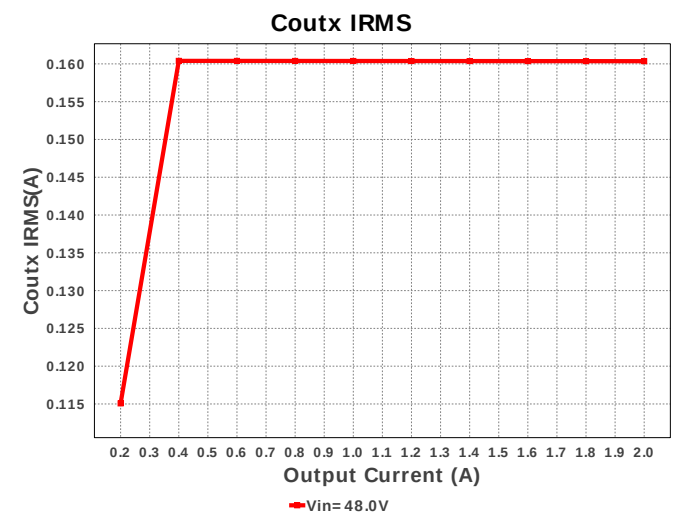
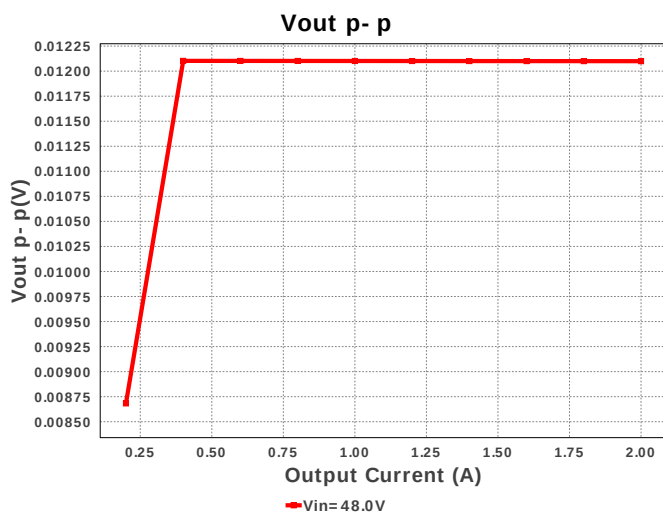
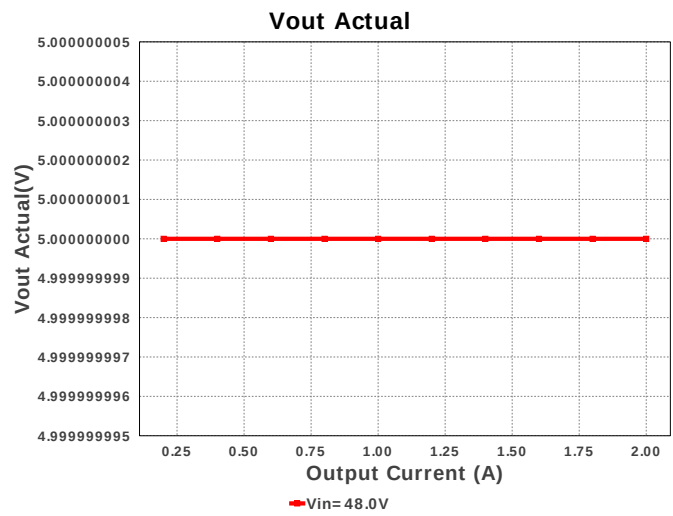
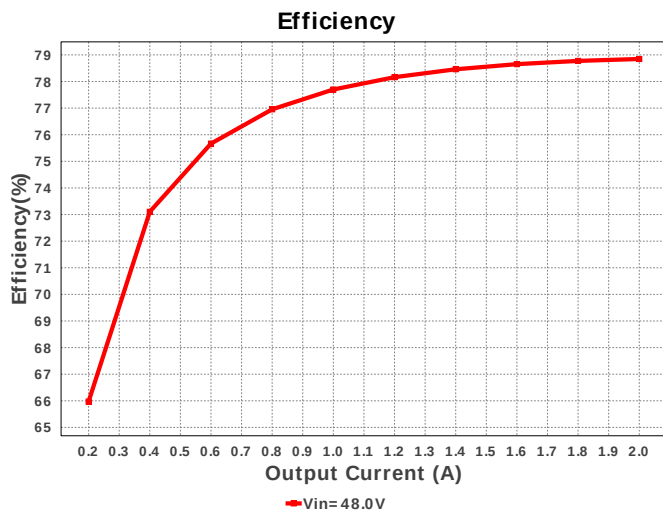
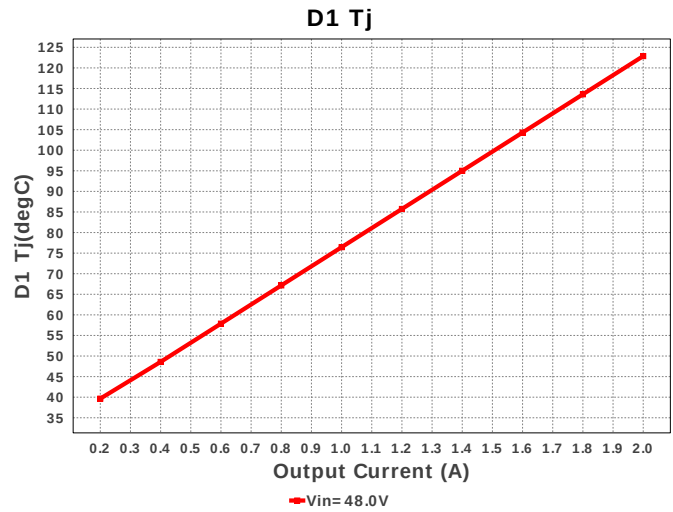
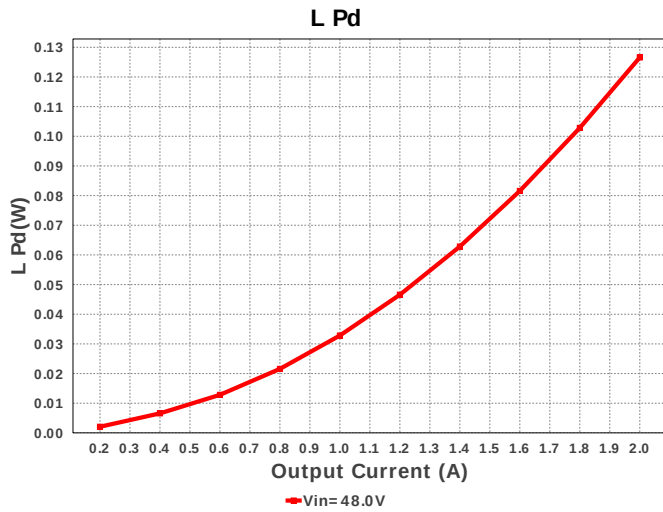


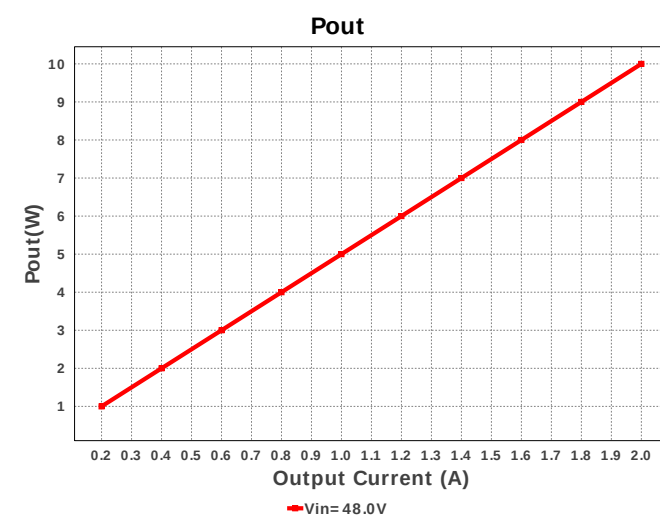
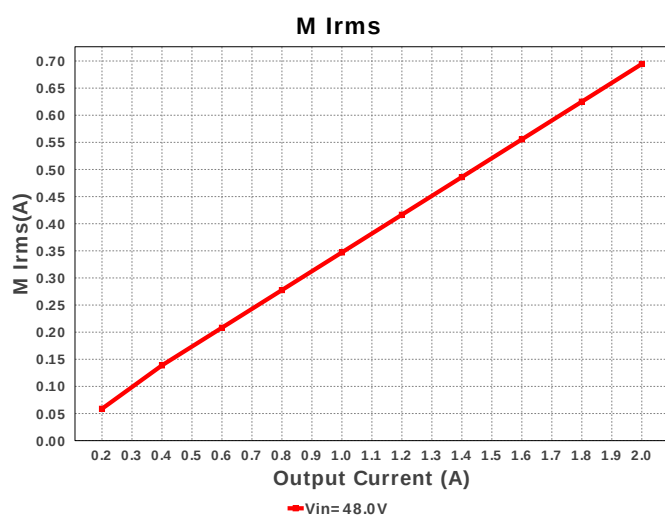
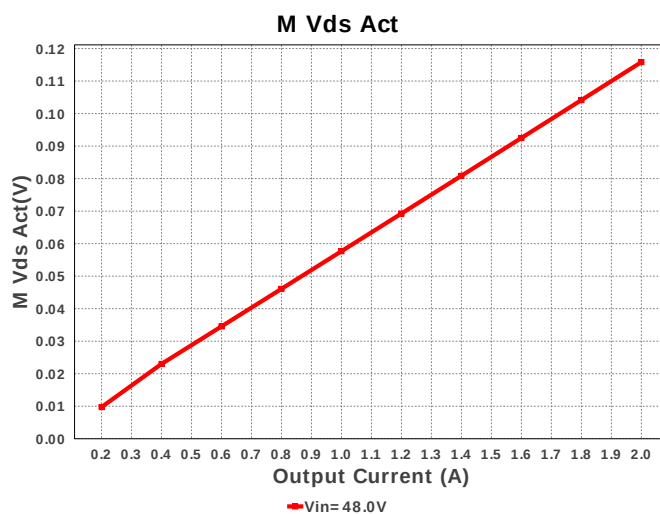
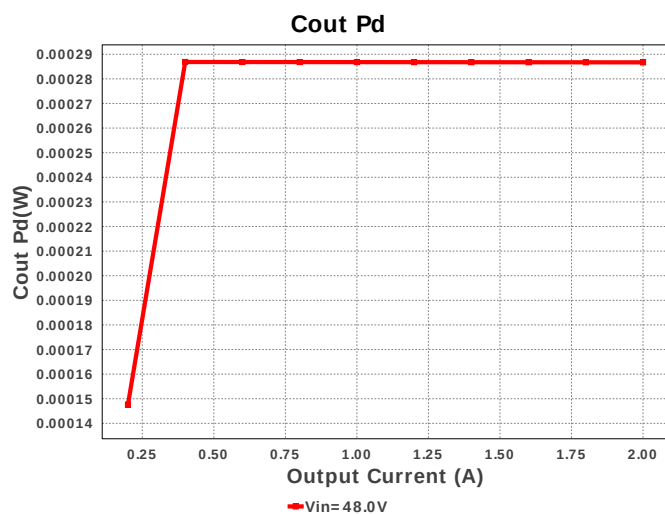
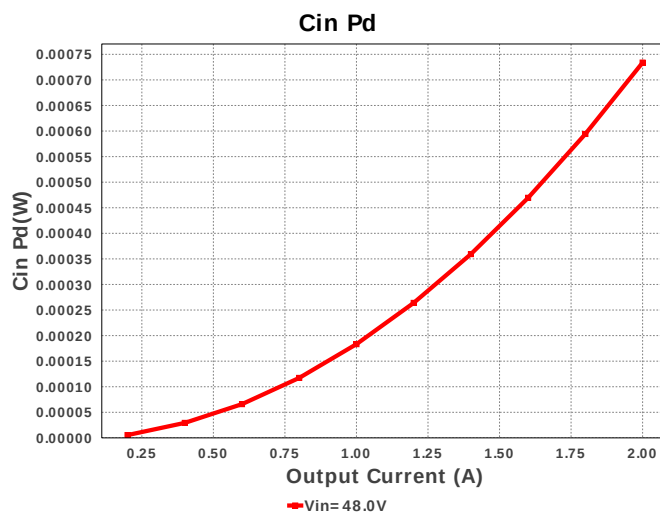
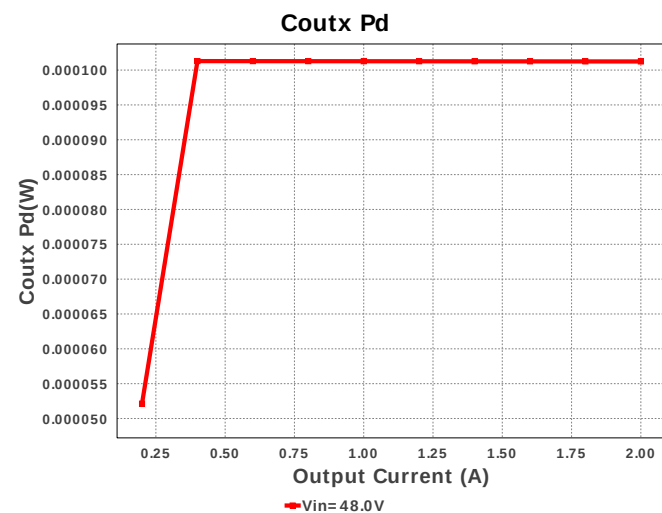


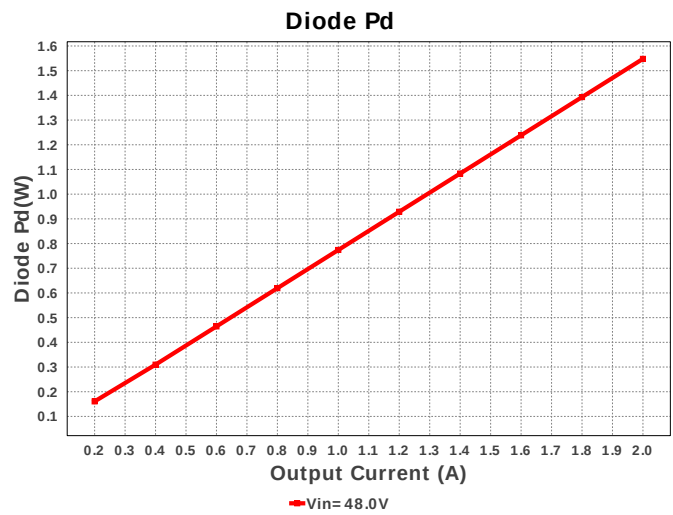
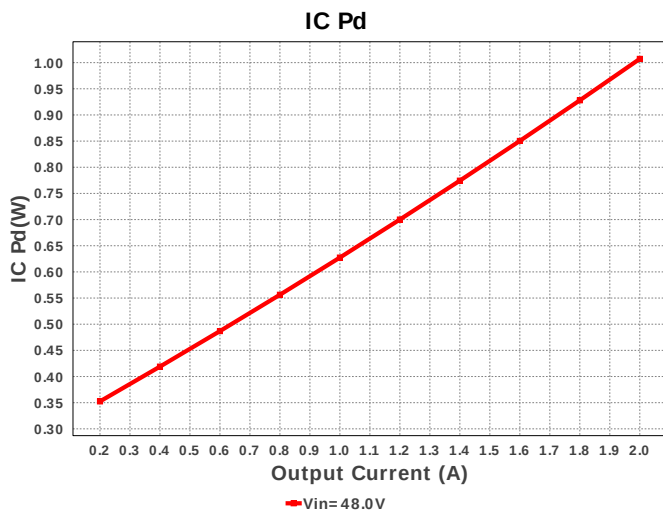
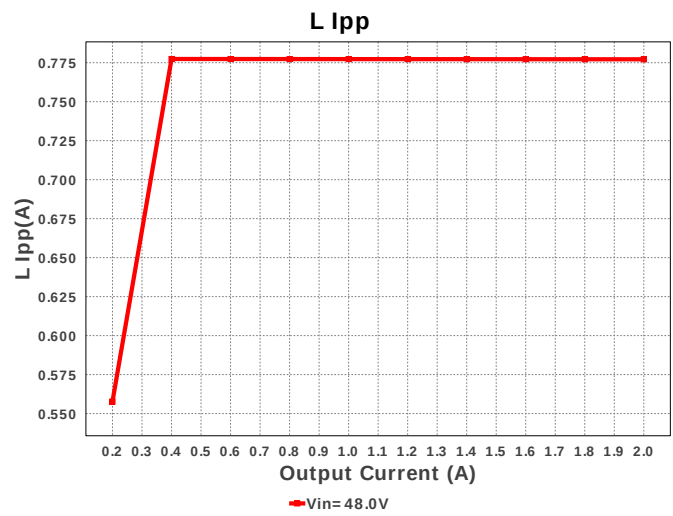
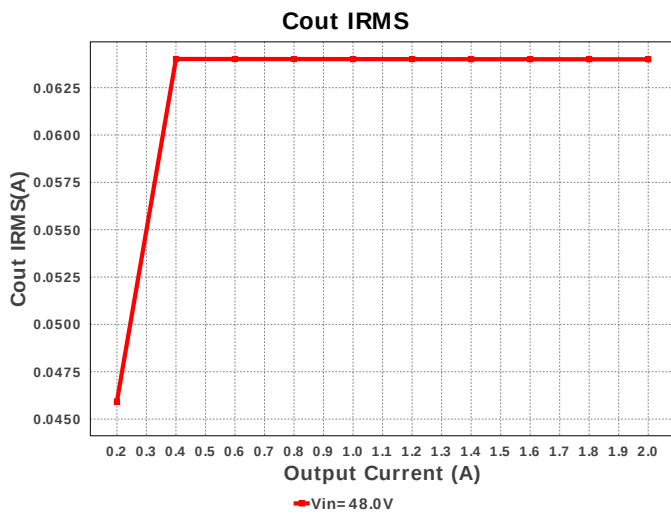
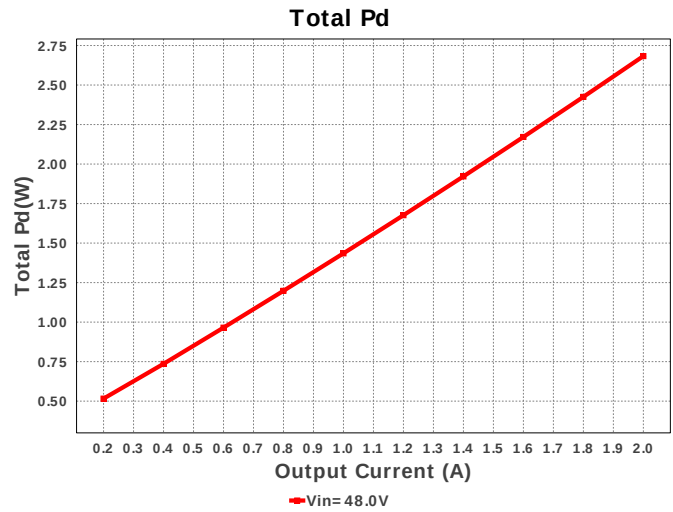
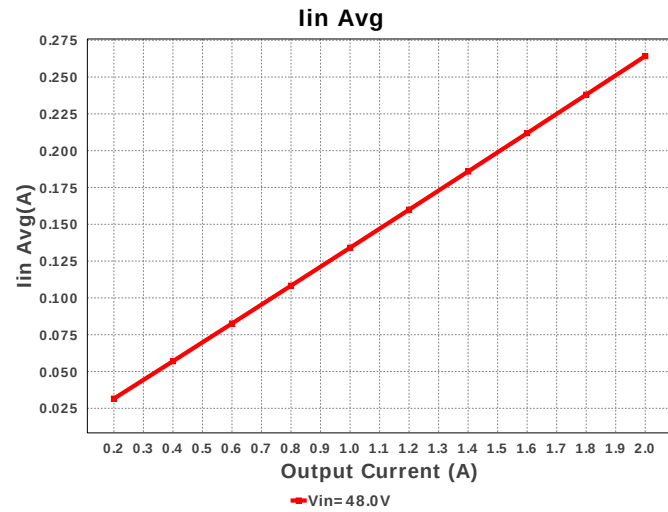
Device = LMR16030SDDAR
Topology = Buck
Created = 7/19/16 8:22:40 PM
BOM Cost = \$3.32
BOM Count = 11
Total Pd = 2.68W

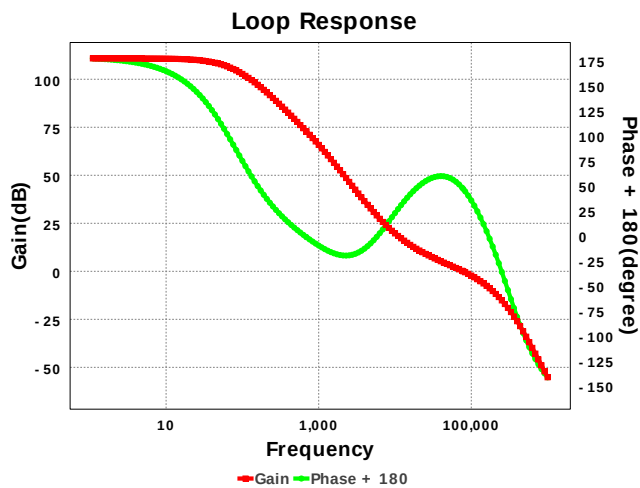
#	Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
11. U1		Texas Instruments	LMR16030SDDAR	Switcher	1	\$1.95	 DDA0008E_N 57 mm ²











Operating Values

#	Name	Value	Category	Description
1.	Cin IRMS	651.298 mA	Current	Input capacitor RMS ripple current
2.	Cout IRMS	63.999 mA	Current	Output capacitor RMS ripple current
3.	Coutx IRMS	160.356 mA	Current	Output capacitor_x RMS ripple current
4.	IC Ipk	2.389 A	Current	Peak switch current in IC
5.	Iin Avg	264.24 mA	Current	Average input current
6.	L Ipp	777.19 mA	Current	Peak-to-peak inductor ripple current
7.	M1 Irms	694.515 mA	Current	Q lavg
8.	BOM Count	11	General	Total Design BOM count
9.	FootPrint	318.0 mm ²	General	Total Foot Print Area of BOM components
10.	Frequency	665.346 kHz	General	Switching frequency
11.	IC Tolerance	18.0 mV	General	IC Feedback Tolerance
12.	M Vds Act	115.811 mV	General	Voltage drop across the MosFET
13.	Pout	10.0 W	General	Total output power
14.	Total BOM	\$3.32	General	Total BOM Cost
15.	D1 Tj	122.87 degC	Op_Point	D1 junction temperature
16.	Vout Actual	5.0 V	Op_Point	Vout Actual calculated based on selected voltage divider resistors
17.	Vout OP	5.0 V	Op_Point	Operational Output Voltage
18.	Cross Freq	83.404 kHz	Op_point	Bode plot crossover frequency
19.	Duty Cycle	12.058 %	Op_point	Duty cycle
20.	Efficiency	78.846 %	Op_point	Steady state efficiency
21.	IC Tj	72.302 degC	Op_point	IC junction temperature
22.	ICThetaJA	42.0 degC/W	Op_point	IC junction-to-ambient thermal resistance
23.	IOUT_OP	2.0 A	Op_point	Iout operating point
24.	Phase Marg	42.544 deg	Op_point	Bode Plot Phase Margin
25.	VIN_OP	48.0 V	Op_point	Vin operating point
26.	Vout p-p	12.099 mV	Op_point	Peak-to-peak output ripple voltage
27.	Cin Pd	733.847 μW	Power	Input capacitor power dissipation
28.	Cout Pd	286.713 μW	Power	Output capacitor power dissipation
29.	Coutx Pd	101.236 μW	Power	Output capacitor_x power loss
30.	Diode Pd	1.548 W	Power	Diode power dissipation
31.	IC Pd	1.007 W	Power	IC power dissipation
32.	L Pd	126.581 mW	Power	Inductor power dissipation
33.	Total Pd	2.683 W	Power	Total Power Dissipation
34.	Vout Tolerance	4.158 %		Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable

Design Inputs

#	Name	Value	Description
1.	Iout	2.0	Maximum Output Current
2.	Iout1	2.0	Output Current #1
3.	VinMax	48.0	Maximum input voltage
4.	VinMin	48.0	Minimum input voltage
5.	Vout	5.0	Output Voltage
6.	Vout1	5.0	Output Voltage #1
7.	Vout2	1.0	Output Voltage #2
8.	base_pn	LMR16030S	Base Product Number
9.	source	DC	Input Source Type
10.	Ta	30.0	Ambient temperature

Design Assistance

1. **LMR16030S** Product Folder : <http://www.ti.com/product/LMR16030> : contains the data sheet and other resources.

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