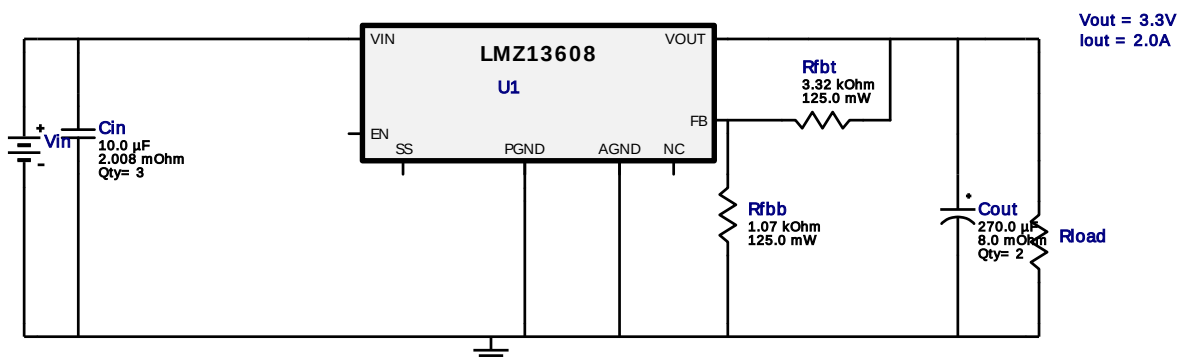
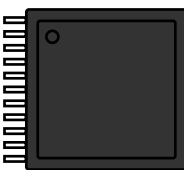


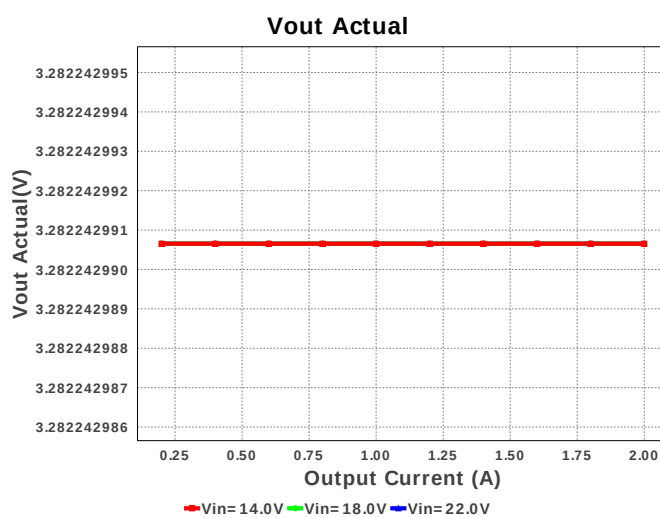
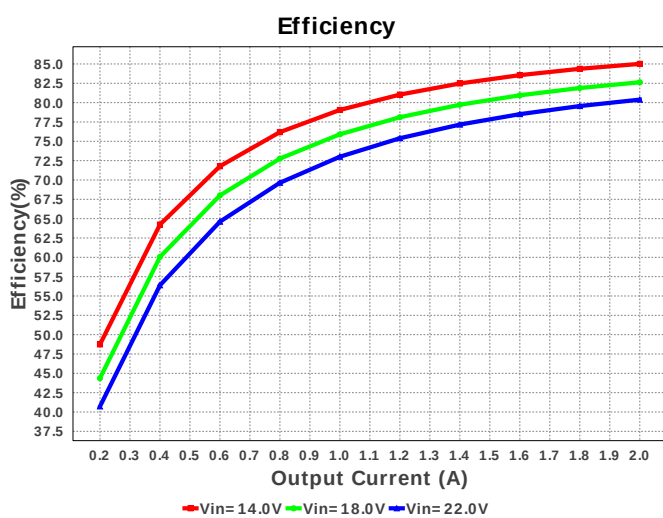
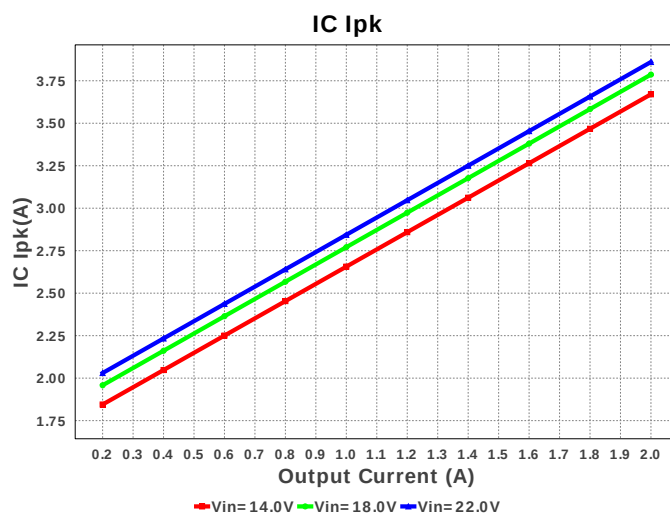
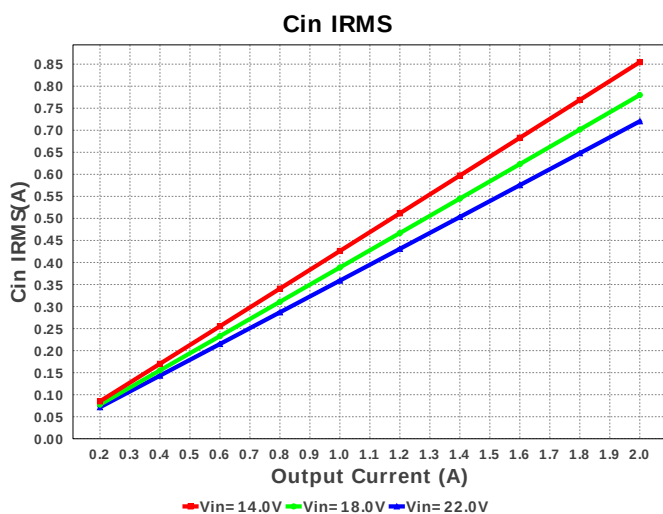
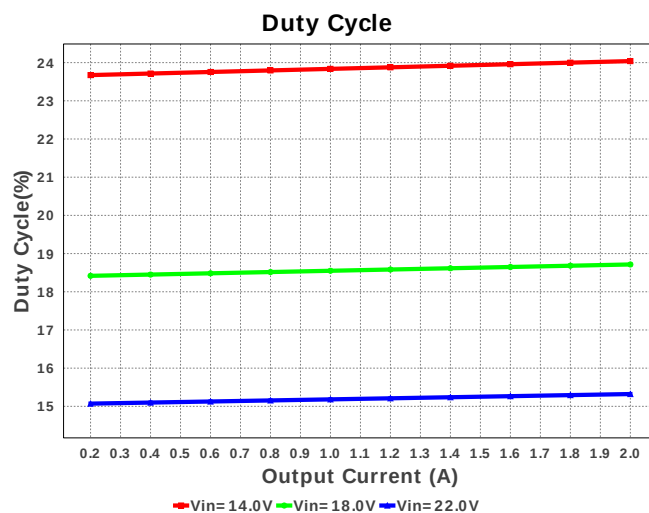
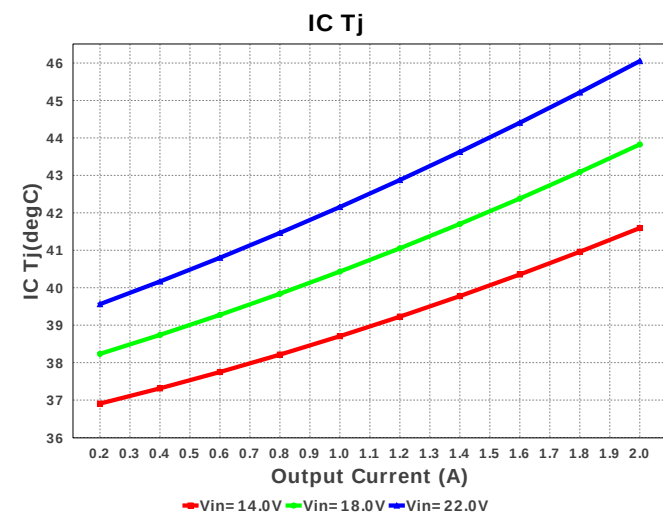
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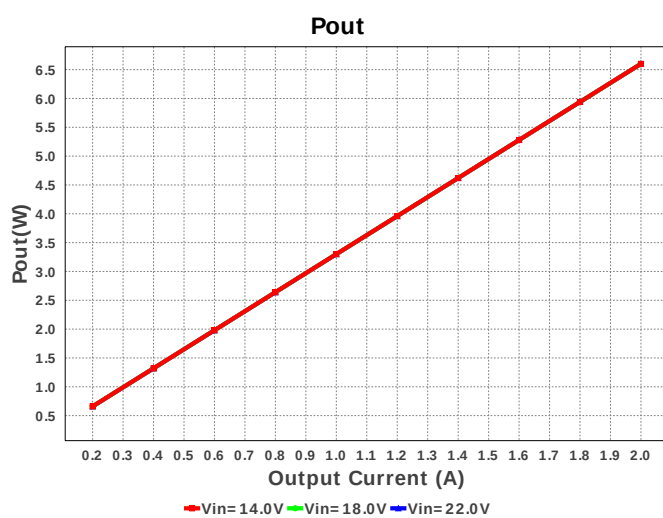
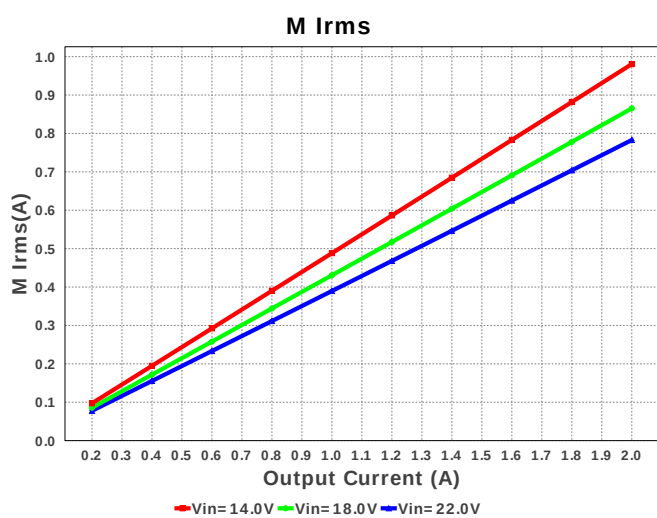
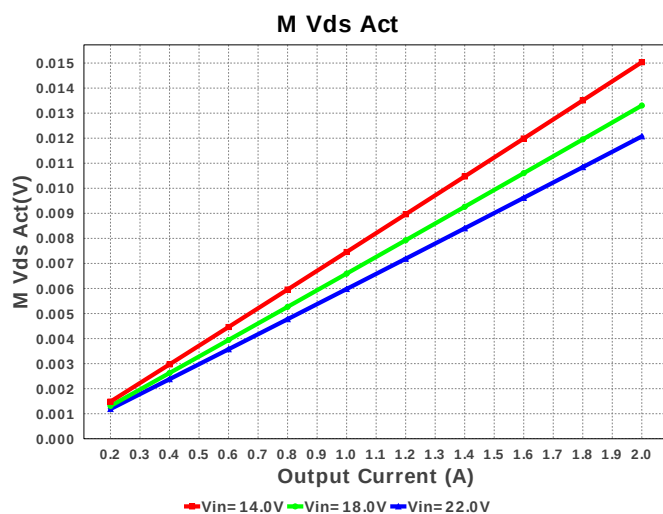
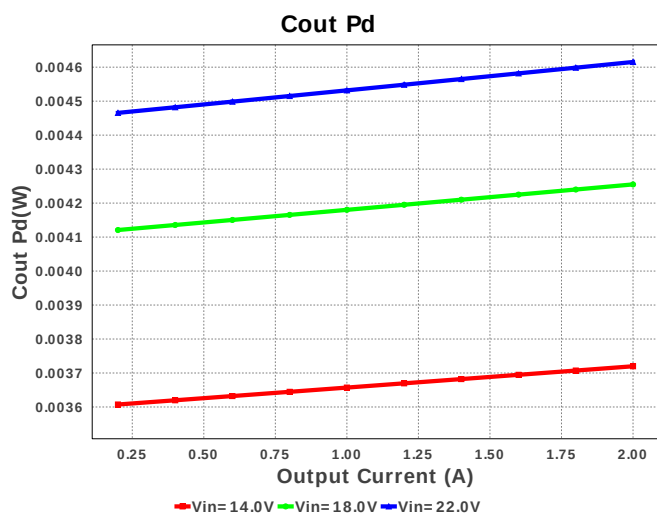
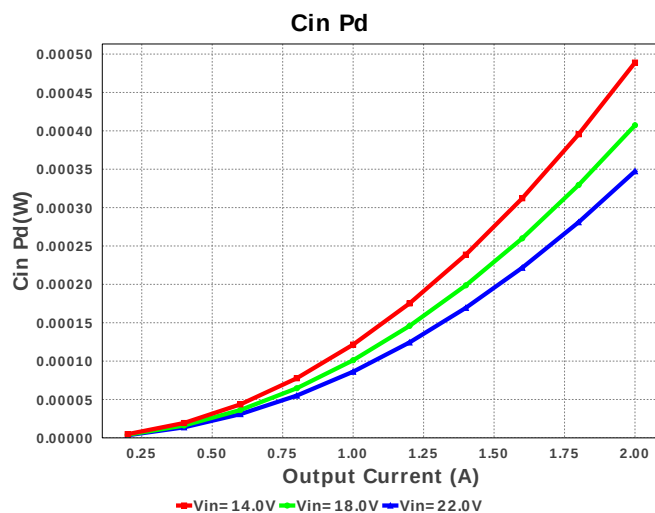
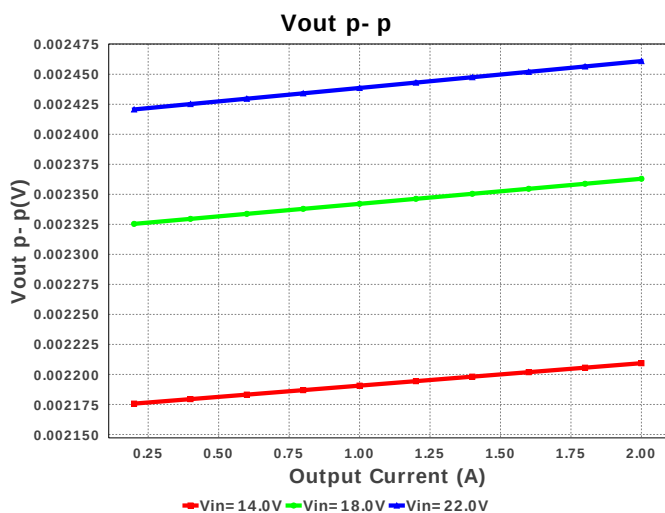
Design : 4737567/93 LMZ13608TZ/NOPB
LMZ13608TZ/NOPB 14.0V-22.0V to 3.30V @ 2.0A

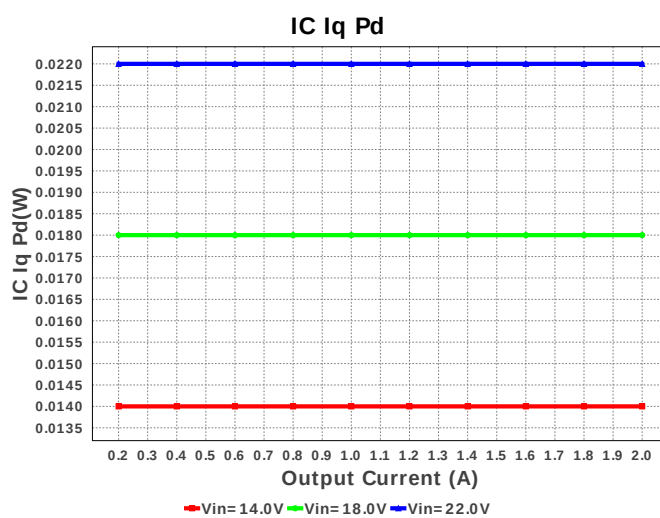
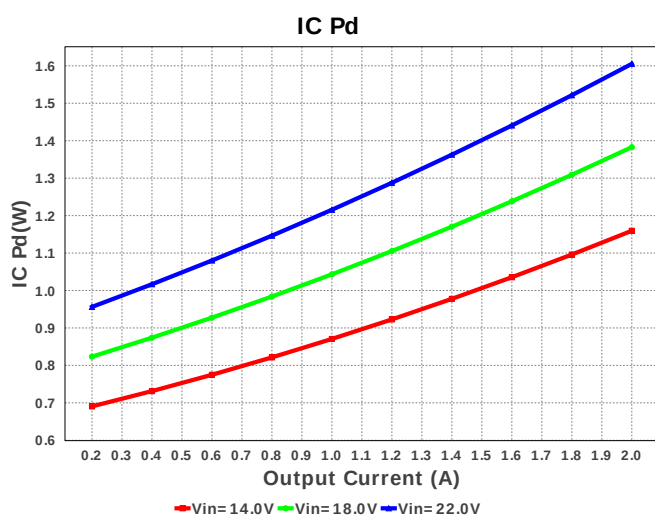
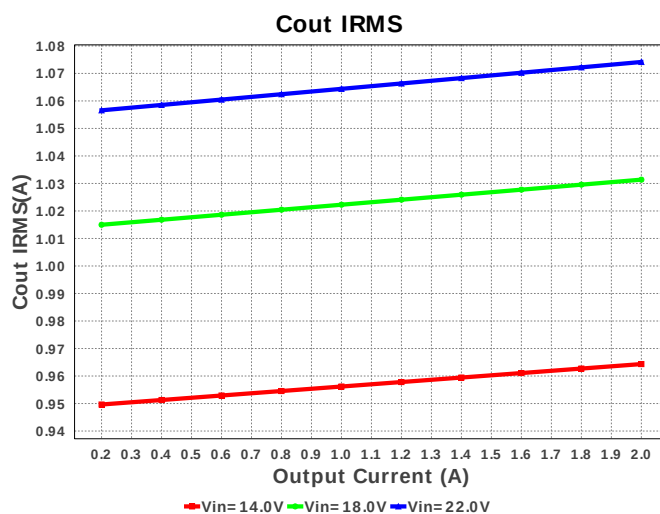
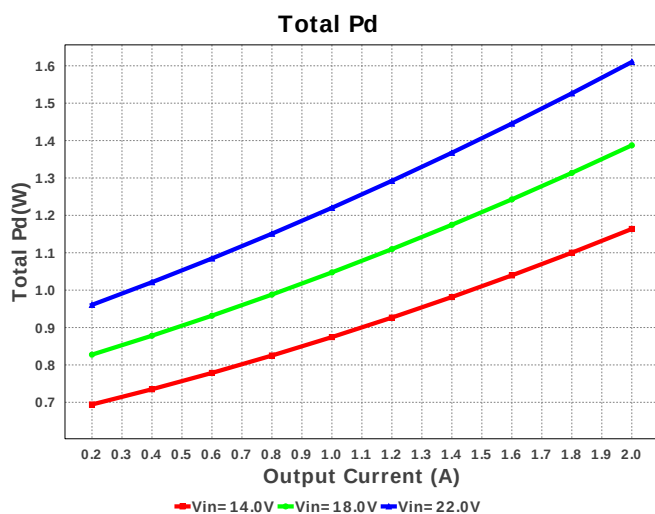
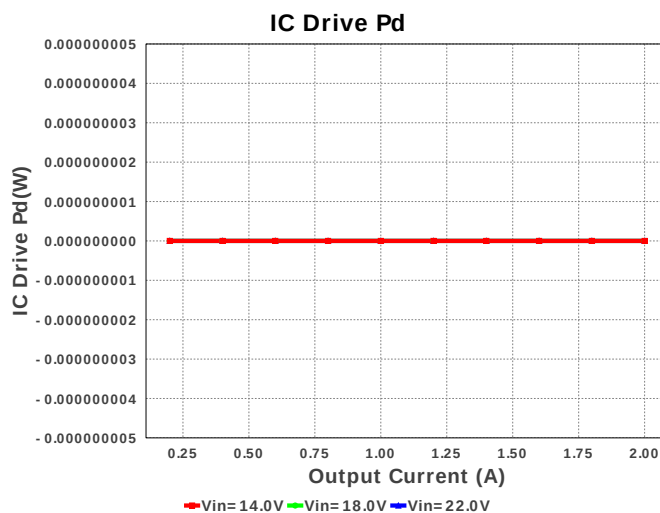
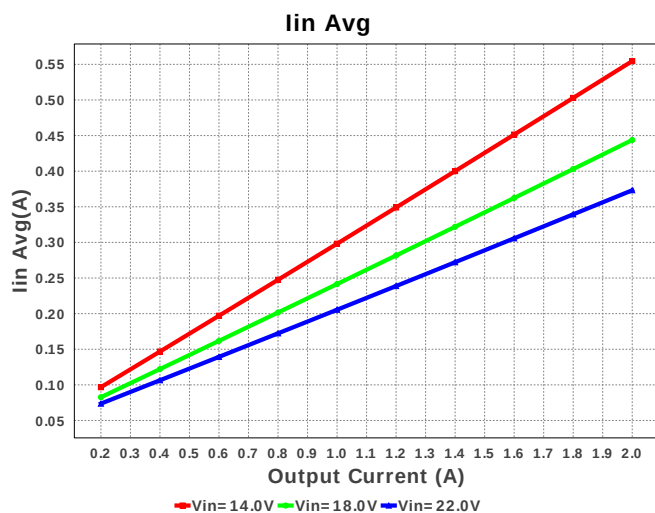


Electrical BOM

#	Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
1.	Cin	MuRata	GRM32ER7YA106KA12L Series= X7R	Cap= 10.0 uF ESR= 2.008 mOhm VDC= 35.0 V IRMS= 4.6772 A	3	\$0.22	 1210_280 15 mm ²
2.	Cout	Panasonic	16SVPG270M Series= ?	Cap= 270.0 uF ESR= 8.0 mOhm VDC= 16.0 V IRMS= 5.8 A	2	\$0.70	 CAPSMT_62_C10 74 mm ²
3.	Rfbb	Panasonic	ERJ-6ENF1071V Series= ERJ-6E	Res= 1.07 kOhm Power= 125.0 mW Tolerance= 1.0%	1	\$0.01	 0805 7 mm ²
4.	Rf1t	Panasonic	ERJ-6ENF3321V Series= ERJ-6E	Res= 3.32 kOhm Power= 125.0 mW Tolerance= 1.0%	1	\$0.01	 0805 7 mm ²
5.	U1	Texas Instruments	LMZ13608TZ/NOPB	Switcher	1	\$12.80	 TZA011A 342 mm ²







Operating Values

#	Name	Value	Category	Description
1.	Cin IRMS	720.394 mA	Current	Input capacitor RMS ripple current
2.	Cout IRMS	1.074 A	Current	Output capacitor RMS ripple current
3.	IC IpK	3.86 A	Current	Peak switch current in IC
4.	Iin Avg	373.19 mA	Current	Average input current
5.	M1 Irms	782.86 mA	Current	Q lavg
6.	BOM Count	8	General	Total Design BOM count
7.	FootPrint	547.0 mm ²	General	Total Foot Print Area of BOM components
8.	Frequency	350.0 kHz	General	Switching frequency
9.	IC Tolerance	20.0 mV	General	IC Feedback Tolerance
10.	M Vds Act	12.073 mV	General	Voltage drop across the MosFET
11.	Pout	6.6 W	General	Total output power

#	Name	Value	Category	Description
12.	Total BOM	\$14.88	General	Total BOM Cost
13.	Vout Actual	3.282 V	Op_Point	Vout Actual calculated based on selected voltage divider resistors
14.	Vout OP	3.3 V	Op_Point	Operational Output Voltage
15.	Cross Freq	10.604 kHz	Op_point	Bode plot crossover frequency
16.	Duty Cycle	15.322 %	Op_point	Duty cycle
17.	Efficiency	80.388 %	Op_point	Steady state efficiency
18.	IC Tj	46.052 degC	Op_point	IC junction temperature
19.	ICThetaJA	10.0 degC/W	Op_point	IC junction-to-ambient thermal resistance
20.	IOUT_OP	2.0 A	Op_point	Iout operating point
21.	Phase Marg	46.013 deg	Op_point	Bode Plot Phase Margin
22.	VIN_OP	22.0 V	Op_point	Vin operating point
23.	Vout p-p	2.461 mV	Op_point	Peak-to-peak output ripple voltage
24.	Cin Pd	347.362 μ W	Power	Input capacitor power dissipation
25.	Cout Pd	4.615 mW	Power	Output capacitor power dissipation
26.	IC Drive Pd	0.0 W	Power	Driver power dissipation
27.	IC Iq Pd	22.0 mW	Power	IC Iq Pd
28.	IC Pd	1.605 W	Power	IC power dissipation
29.	Total Pd	1.61 W	Power	Total Power Dissipation
30.	Vout Tolerance	4.066 %		Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable

Design Inputs

#	Name	Value	Description
1.	Iout	2.0	Maximum Output Current
2.	VinMax	22.0	Maximum input voltage
3.	VinMin	14.0	Minimum input voltage
4.	Vout	3.3	Output Voltage
5.	base_pn	LMZ13608	Base Product Number
6.	source	DC	Input Source Type
7.	Ta	30.0	Ambient temperature

Design Assistance

1. The Modules are very easy to use and just need a basic design using a resistor divider at the feedback and input and output caps to work. To design for UVLO you could click on the drop down menu in the 'Change Inputs' menu and select the 'UVLO Enabled Design'. The internal softstart time is set at 1.6mSec. If a longer softstart time is desired, you could change the preset to the desired amount and click on 'Submit'. Webench will then add an external softstartcap to the schematic. For designs requiring more than 8A of load current, multiple LMZ23608 parts can be used by connecting their 'SH' pins together. The 'Master' LMZ23608 is set by connecting the resistor divider from feedback to the output. The slaves have their feedback pins open.

2. **LMZ13608 Product Folder** : <http://www.ti.com/product/LMZ13608> : contains the data sheet and other resources.

Texas Instruments' WEBENCH simulation tools attempt to recreate the performance of a substantially equivalent physical implementation of the design. Simulations are created using Texas Instruments' published specifications as well as the published specifications of other device manufacturers. While Texas Instruments does update this information periodically, this information may not be current at the time the simulation is built. Texas Instruments does not warrant the accuracy or completeness of the specifications or any information contained therein. Texas Instruments does not warrant that any designs or recommended parts will meet the specifications you entered, will be suitable for your application or fit for any particular purpose, or will operate as shown in the simulation in a physical implementation. Texas Instruments does not warrant that the designs are production worthy.

You should completely validate and test your design implementation to confirm the system functionality for your application prior to production.

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