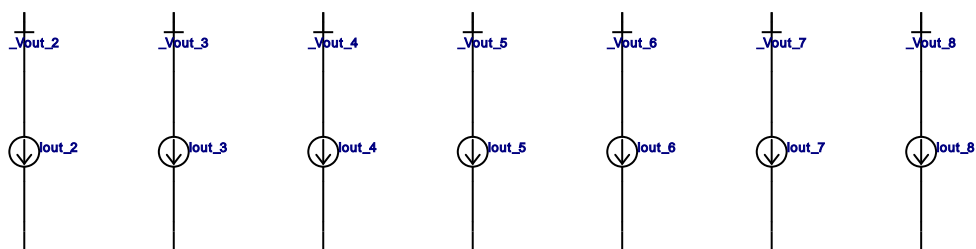
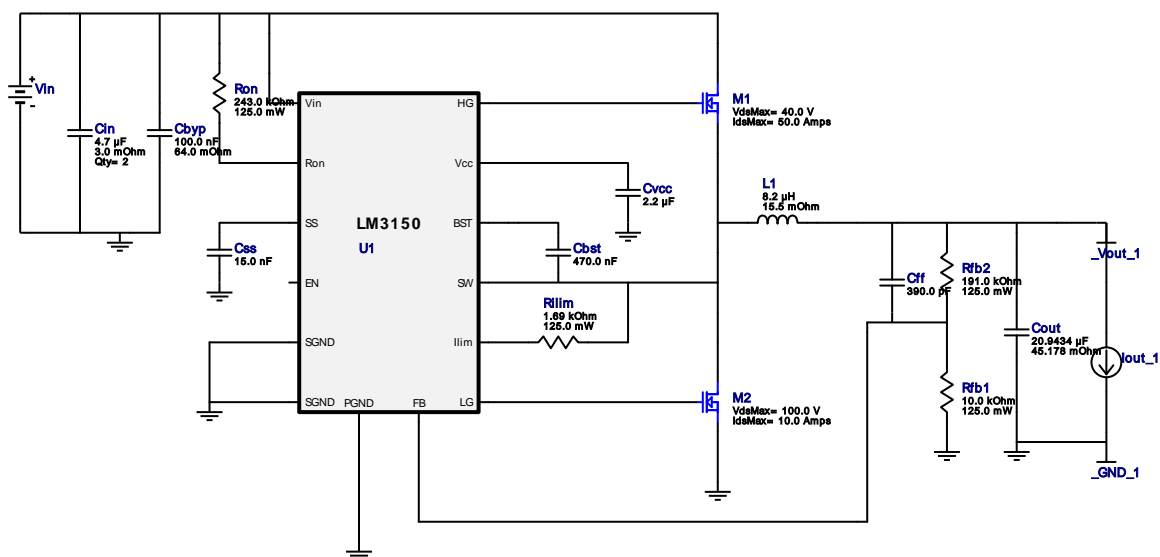


Bottom Layer



Mechanical BOM

#	WEBENCH Name	Altrium Comp Type	Part Name	FootPrint
No Mechanical BOM Data				

Electrical BOM

#	Component Name(s)	Part Number	Manufacture	Qty	Unit Price	Top View
1.	Rfb2	ERJ-6ENF1913V	Panasonic	1	\$0.1	 0805 0.0
2.	L1	SRP1270-8R2M	Bourns	1	\$1.44	 SRP1270 0.0
3.	Cvcc	EMK212B7225KG-T	Taiyo Yuden	1	\$0.3	 0805 0.0
4.	M1	CSD18504Q5A	Texas Instruments	1	\$1.53	 TRANS_NexFET_Q5A 0.0
5.	Rilim	ERJ-6ENF1691V	Panasonic	1	\$0.1	 0805 0.0
6.	Cin	GRM31CR71H475KA12L	MuRata	2	\$0.32	 1206 0.0
7.	Cbst	EMK212B7474KD-T	Taiyo Yuden	1	\$0.28	 0805 0.0
8.	Rfb1	ERJ-6ENF1002V	Panasonic	1	\$0.1	 0805 0.0
9.	Css	CC0805KRX7R9BB153	Yageo America	1	\$0.1	 0805 0.0
10.	M2	CSD19534Q5A	Texas Instruments	1	\$1.64	 TRANS_NexFET_Q5A 0.0
11.	Ron	ERJ-6ENF2433V	Panasonic	1	\$0.1	 0805 0.0
12.	Cff	CC0805KRX7R9BB391	Yageo America	1	\$0.1	 0805 0.0
13.	Cout	CUSTOM	CUSTOM	1	\$0.0	CUSTOM 0.0
14.	VIN+,VIN-,VOUT+,VOUT-	1502-2	Keystone	4	\$0.34	0.0
15.	U1	LM3150MH/NOPB	Texas Instruments	1	\$3.86	 MXA14A 0.0
16.	Cbyp	C0805C104K5RACTU	Kemet	1	\$0.1	 0805 0.0
17.	PC Board	551600142-002	Texas Instruments	1	\$0.0	0.0

Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of C_{in} and C_{out} , and the inductance and DC resistance of $L1$ before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

Soldering Component to Board

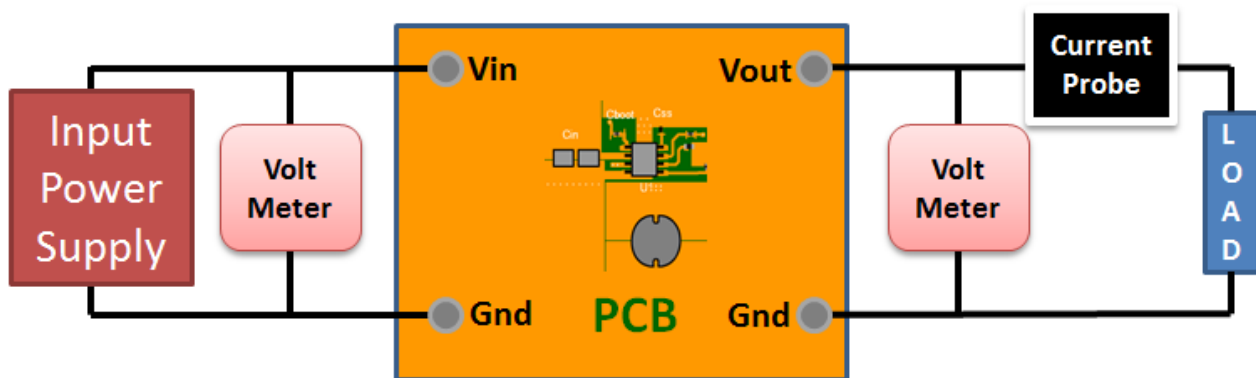
If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab down to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 18.0V and set the input supply's current limit to zero. With the input supply off connect up the input supply to V_{in} and GND. Connect a digital volt meter and a load if needed to set the minimum load of the design from V_{out} and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between V_{in} and GND, a load is connected between V_{out} and GND and a current meter is connected in series between V_{out} and the load. The load must be able to handle at least rated output power + 50% (7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.



Design Inputs

#	Name	Value	Description
1.	I _{out}	5.45 A	Maximum Output Current
2.	VinMax	32.0 V	Maximum input voltage
3.	VinMin	18.0 V	Minimum input voltage
4.	Vout	12.0 V	Output Voltage
5.	base_pn	LM3150	Base Product Number
6.	source	DC	Input Source Type
7.	Ta	30.0 degC	Ambient temperature

Design Assistance

1. **LM3150** Product Folder : <http://www.ti.com/product/LM3150> : contains the data sheet and other resources.

Texas Instruments' WEBENCH simulation tools attempt to recreate the performance of a substantially equivalent physical implementation of the design. Simulations are created using Texas Instruments' published specifications as well as the published specifications of other device manufacturers. While Texas Instruments does update this information periodically, this information may not be current at the time the simulation is built. Texas Instruments does not warrant the accuracy or completeness of the specifications or any information contained therein. Texas Instruments does not warrant that any designs or recommended parts will meet the specifications you entered, will be suitable for your application or fit for any particular purpose, or will operate as shown in the simulation in a physical implementation. Texas Instruments does not warrant that the designs are production worthy.

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